



## **Icera and Magma Collaborate on 28-nm High-Performance, Low-Power Flow to Accelerate Turnaround time for Next-Generation Soft Modem Chipsets**

Icera adopts Talus 1.2 platform with advanced OCV and clock gating capabilities for improved area efficiency and low power consumption

SAN JOSE, Calif., and Bristol, UK, Dec. 2, 2010 – Magma Design Automation Inc. (Nasdaq: LAVA), a provider of chip design software, and Icera Inc., a pioneer in soft modem chipsets for smartphones and mobile broadband devices, today announced joint development of Icera's 28-nanometer (nm) high-performance, low-power system-on-chip (SoC) design flow for its next-generation chipsets. Icera devices are used by OEMs globally to deliver extremely small, fully software-based multimode 4G LTE/3G/2G cellular modems for smartphones, and mobile broadband devices such as USB sticks, tablets and netbooks. Icera chose Magma's Talus platform because it provides a highly integrated flow that enables Icera to address 28-nm design complexity, improve area efficiency and reduce power consumption while accelerating turnaround time.

"To maintain technical leadership in the cellular modem business, Icera cannot afford to compromise on performance, power consumption or area efficiency," said Peter Hughes, vice president of Silicon Engineering and Operations at Icera. "The advanced technology and tight integration of the Talus platform enable Icera to meet our stringent power and area targets and to reduce time to market for our next-generation 28-nm soft modem chipset."

"The recent enhancements to the Talus platform were developed specifically to address increasing design complexity at the 28-nm node while improving designer productivity dramatically," said Premal Buch, general manager of Magma's Design Implementation Business Unit. "Offering the capacity to process 1 million cells per day, integrating the Tekton-based MX timing engine to ensure correlation and accelerate timing closure, and supporting advanced low-power design techniques, Talus is the clear path to 28-nm and smaller SoCs."

### **Talus 1.2 Increases Implementation Throughput, Decreases Power for Icera Chipsets**

Talus 1.2's new advanced on-chip variation (AOCV) driven optimization significantly reduces pessimistic design margins that are required in traditional flows, and Icera plans to leverage this capability to improve the performance of its soft modem chipsets. Unlike other approaches, this optimization is

performed throughout the Talus flow, providing convergent and robust timing while at the same time reducing any area penalty related to OCV effects. With Talus 1.2 designers can improve silicon correlation and minimize power consumption.

Talus 1.2's advanced clock gating techniques help customers significantly reduce both area and power, particularly in the clock network. This allows the core processing functions to achieve required operating frequencies while remaining within a tight power budget.

In addition, Talus 1.2 offers tight timing correlation throughout the flow and timing sign-off through its new MX timing engine derived from Tekton™, Magma's extremely fast and accurate standalone static timing analyzer. With reliable timing numbers that Talus delivers early in the design process, designers can make better design decisions throughout the flow and have confidence in achieving timing convergence faster.

### **About Icera**

Icera is a fabless semiconductor company, pioneering software-defined modem chipsets for the fast growing smartphone and mobile broadband device markets. Icera technology delivers the highest performance modem solutions with the smallest silicon die size for smartphones, USB dongles, laptops, netbooks. Icera technology supports 4G (LTE), 3G (HSPA) and 2G standards. Founded in 2002, Icera is headquartered in the UK, with design locations in the UK, France, USA and China, with customer engineering and sales offices in Europe, Asia and the USA. For more information, visit the Icera web site at [www.icerasemi.com](http://www.icerasemi.com).

### **About Magma**

Magma's electronic design automation (EDA) software provides the "Fastest Path to Silicon"™ and enables the world's top chip companies to create high-performance integrated circuits (ICs) for cellular telephones, smartphones, electronic games, WiFi, MP3 players, digital video, networking and other electronic applications. Magma products are used in IC implementation, analog/mixed-signal design, analysis, physical verification, circuit simulation and characterization. The company maintains headquarters in San Jose, Calif., and offices throughout North America, Europe, Japan, Asia and India. Magma's stock trades on Nasdaq under the ticker symbol LAVA. Follow Magma on Twitter at [www.Twitter.com/MagmaEDA](http://www.Twitter.com/MagmaEDA) and on Facebook at [www.Facebook.com/Magma](http://www.Facebook.com/Magma). Visit Magma Design Automation on the Web at [www.magma-da.com](http://www.magma-da.com).

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### **Forward-Looking Statements:**

Except for the historical information contained herein, the matters set forth in this press release, including statements that Talus minimizes power, area and turnaround time and other statements about the features and benefits of Magma products, are forward-looking statements within the meaning of the "safe harbor" provisions of the Private Securities Litigation Reform Act of 1995. These forward-looking statements are subject to risks and uncertainties that could cause actual results to differ materially including, but not limited to, Icera's decision to continue using Magma's software; both companies' abilities to keep pace with rapidly changing technology; and their products' abilities to produce desired results. Further discussion of these and other potential risk factors may be found in Magma's public filings with the Securities and Exchange Commission ([www.sec.gov](http://www.sec.gov)). Magma undertakes no additional obligation to update these forward-looking statements.

**Contacts:**

Magma Design Automation Inc.  
Monica Marmie  
Director, Corporate Marketing  
(408) 565-7689  
[mmarmie@magma-da.com](mailto:mmarmie@magma-da.com)

Icera Inc.  
Sally Doherty  
Head of Corporate Communications  
+44 (0)1454 284859  
[sally@icerasemi.com](mailto:sally@icerasemi.com)