

Processing Petabytes per Second with the ATLAS Experiment at the Large Hadron Collider in CERN GPU Technology Conference 2010

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University of Edinburgh

22nd September 2010



Outline

- 1 Introduction to the LHC and ATLAS
 - The Large Hadron Collider (LHC)
 - The ATLAS detector
 - The Higgs Boson
- 2 Areas of study for GPGPU adoption
 - Particle tracking in a magnetic field
 - The ATLAS trigger and data acquisition
 - The worldwide LHC computing grid
- 3 Using GPUs in the high level trigger
 - The Level 2 Trigger
 - The Z finder algorithm
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The Large Hadron Collider

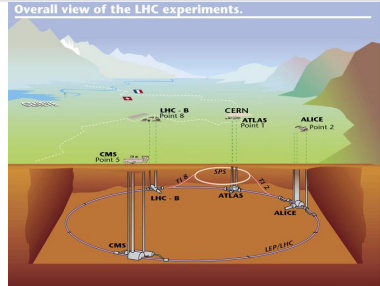


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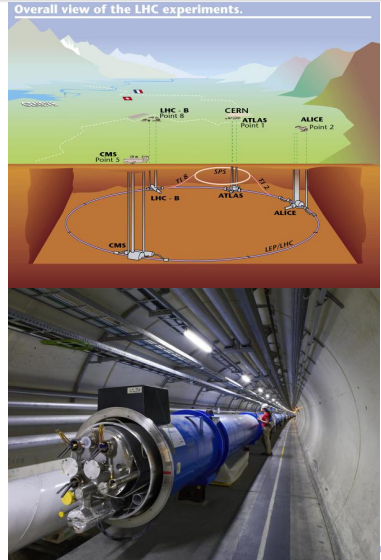
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(27 km circumference)



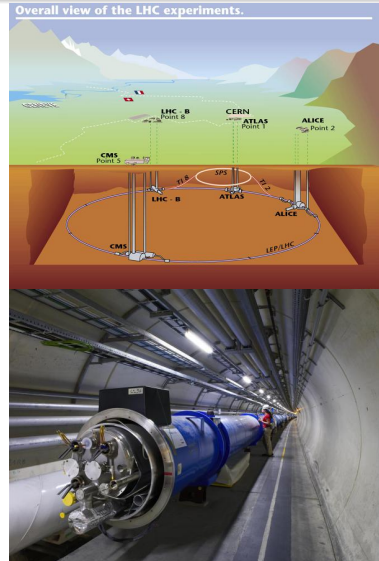
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- Highest energy: protons with 7 TeV
(99.9999991% of speed of light)



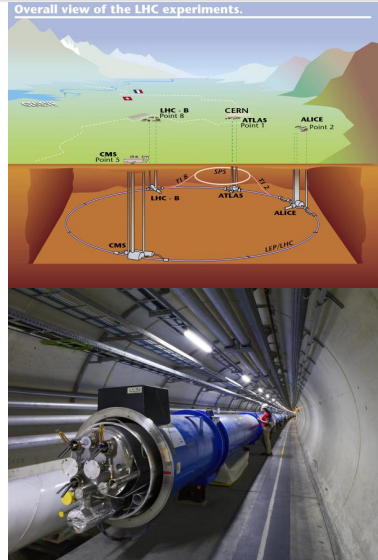
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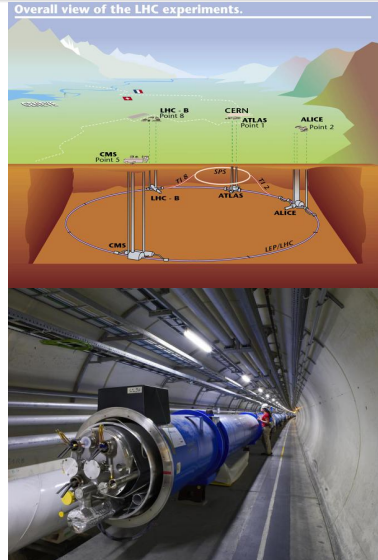
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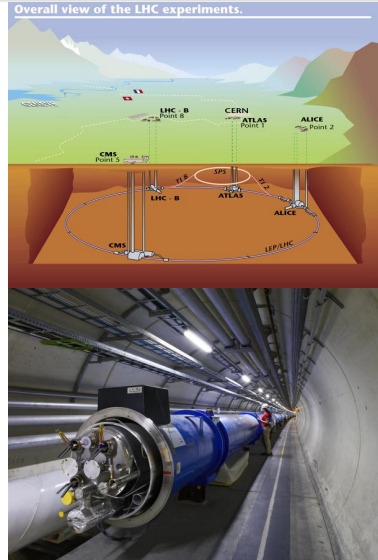
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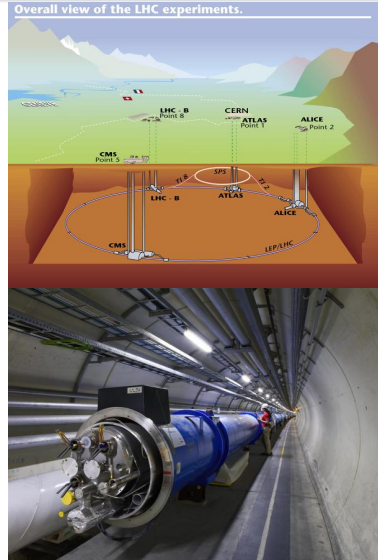
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Attracted Large Media Interest

Large Hadron Collider will not
turn world to goo, promise
scientists - Times Online

Stephen Hawking: Large Hadron
Collider vital for humanity - The
Telegraph

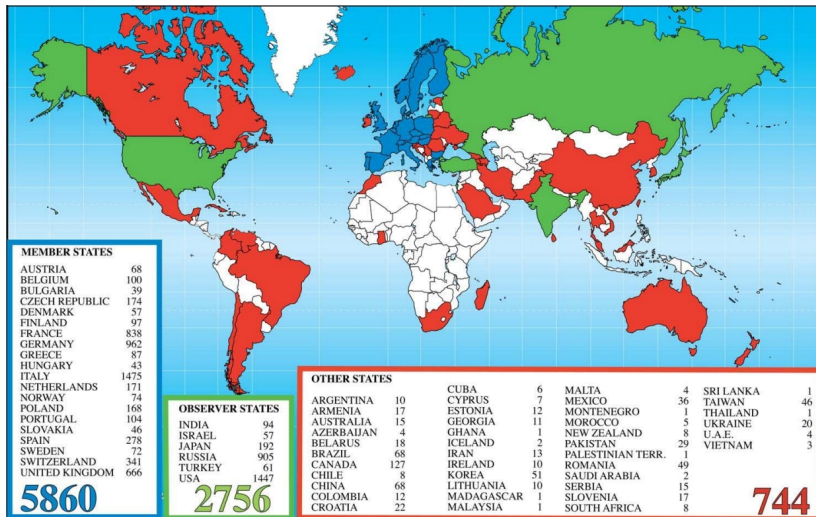
Is the world about to go out
with a bang? - The Herald

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most powerful particle accelerator - The
Guardian

End of the world due in nine
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The ATLAS experiment: up and
atom... - Times Online

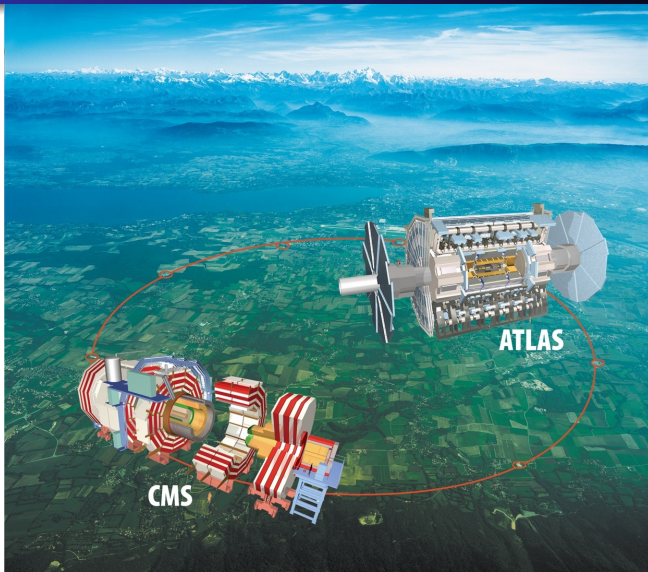
Global CERN project (~10,000 scientists & engineers)



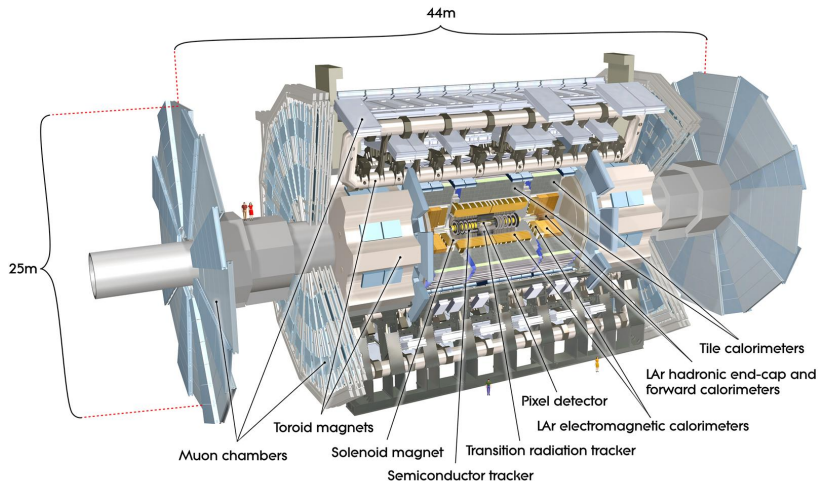
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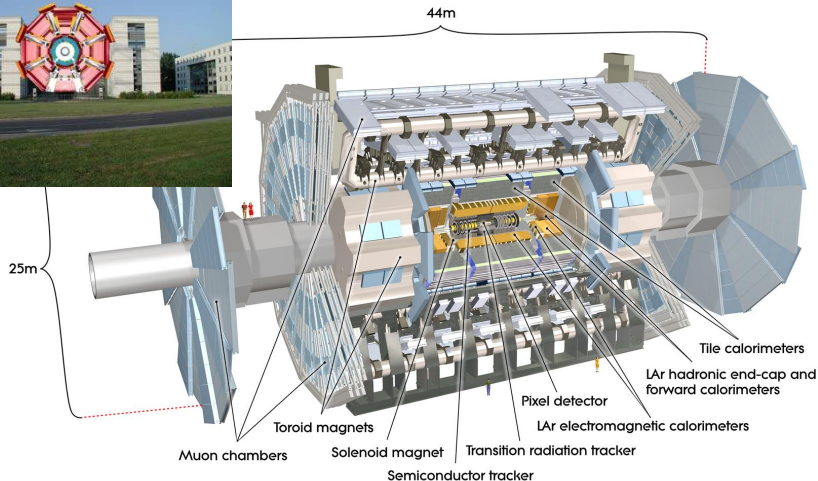
Two of the LHC collision points



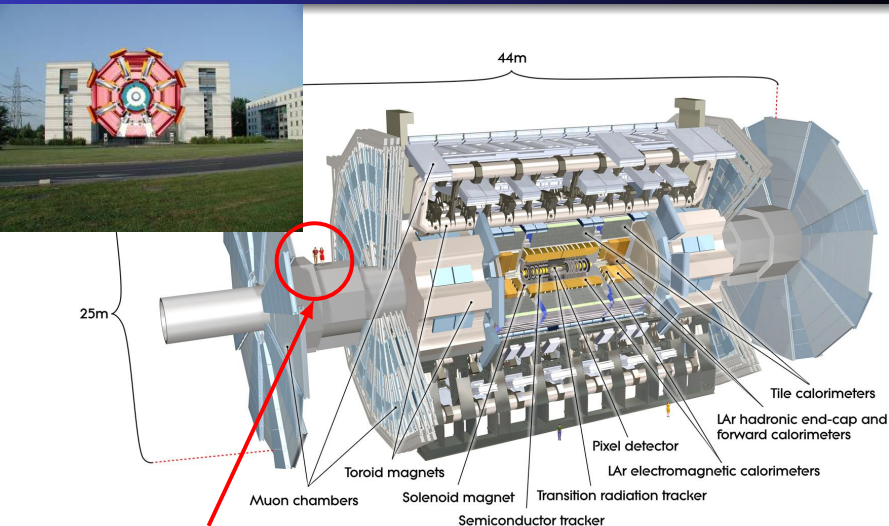
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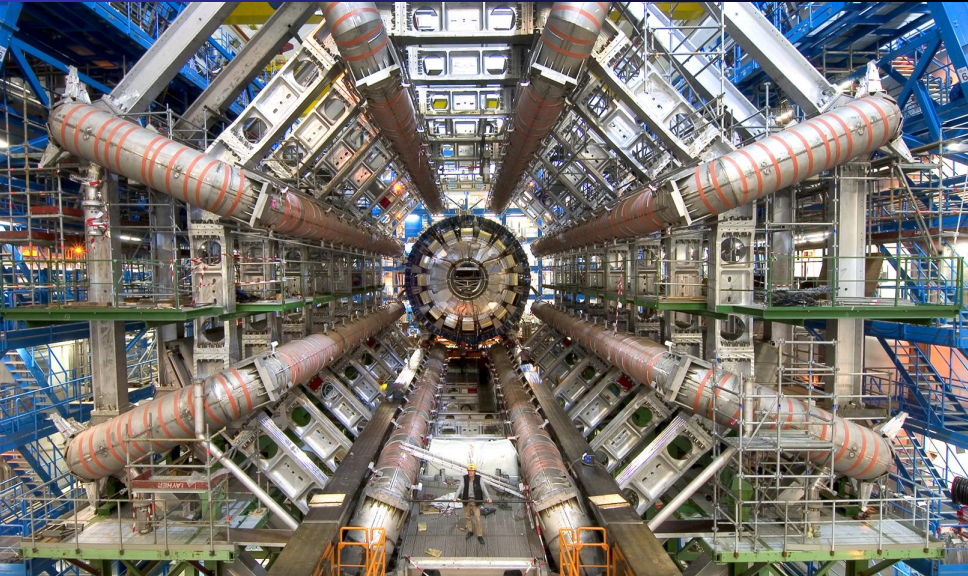


Physicist couple (10^{30} protons)

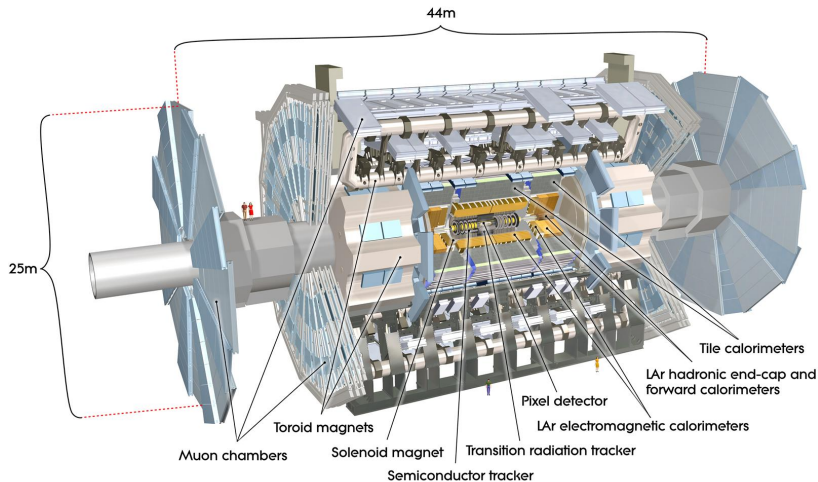
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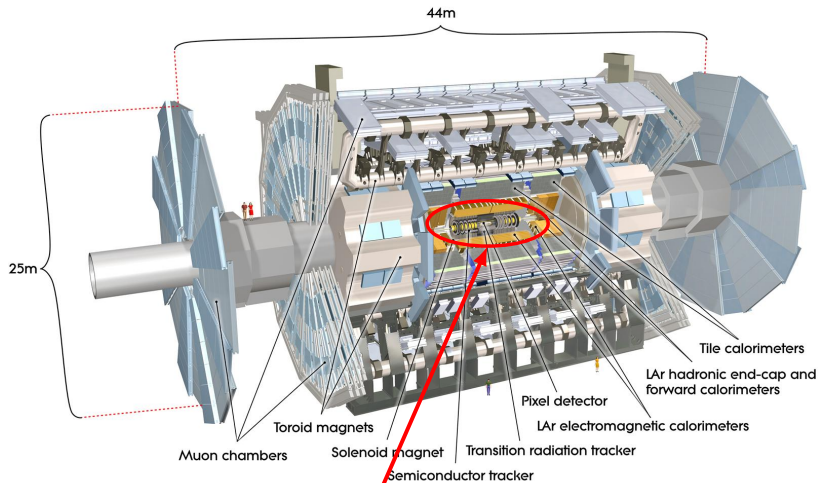
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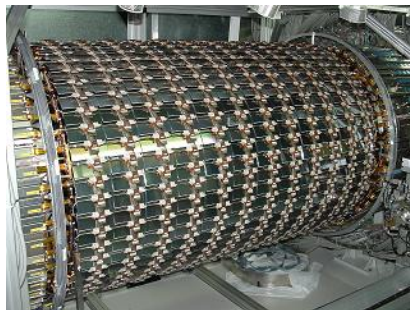
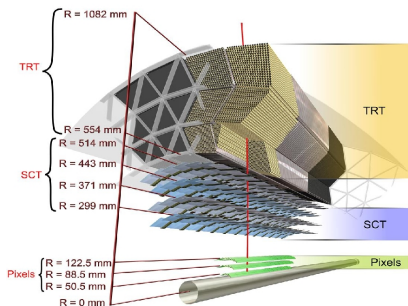
The ATLAS detector



ATLAS Inner Detector

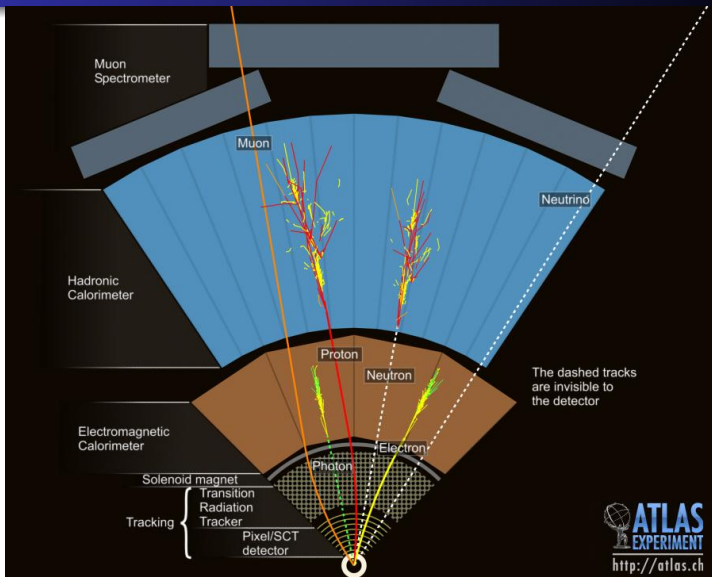
The ATLAS Inner Detector

Detector hits provide space points, used to **track** particles



- Pixels pin-point the particle production vertex
- 80 million readout channels!
- SCT provides large area tracking: 61 m^2 silicon
- 6.3 million readout channels

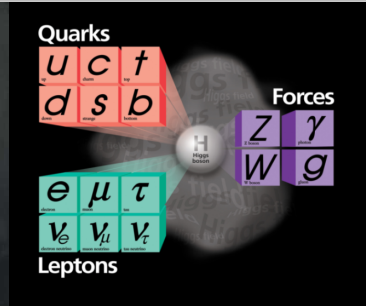
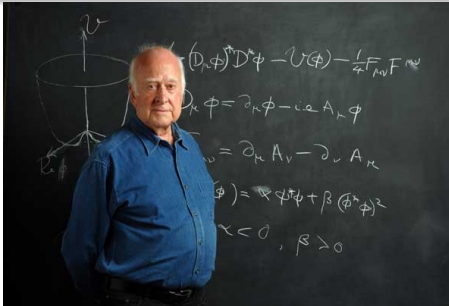
A slice of the detector



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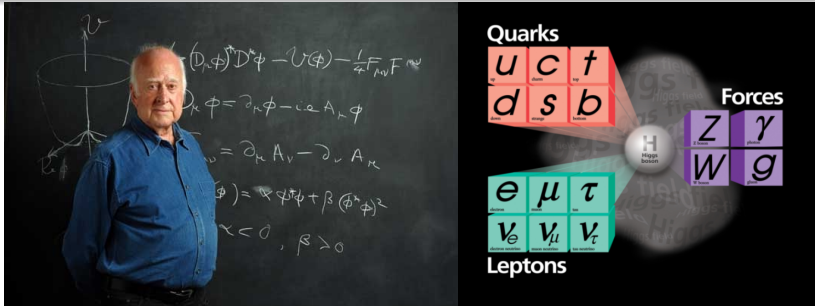
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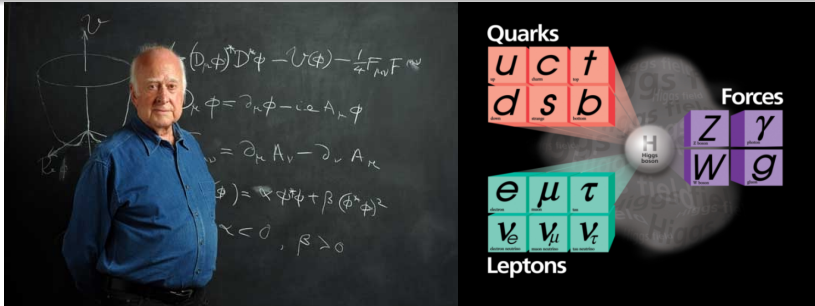
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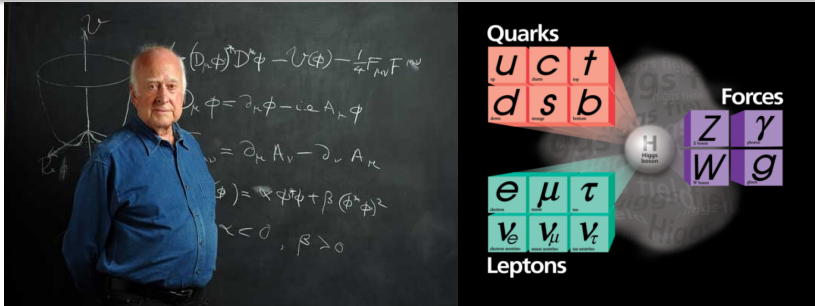
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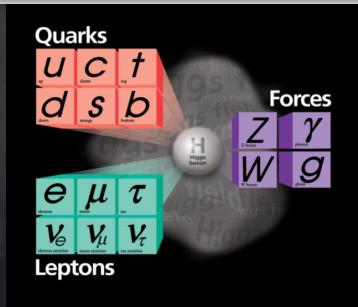
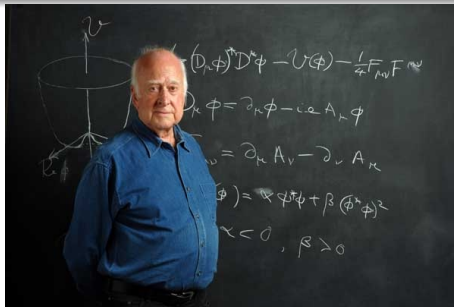
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Gives mass to the leptons, quarks, W and Z force particles

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Gives mass to the leptons, quarks, W and Z force particles
- Requires a new type of particle to exist: the **Higgs boson**

Hunting for the Higgs boson

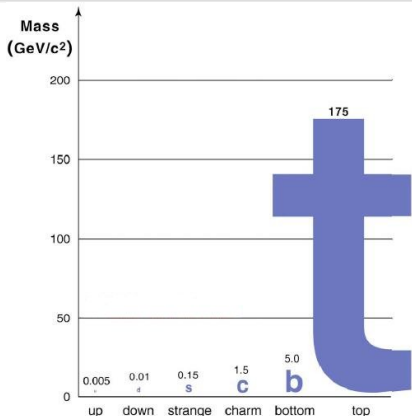
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- Why do particles (and thus matter) have mass?

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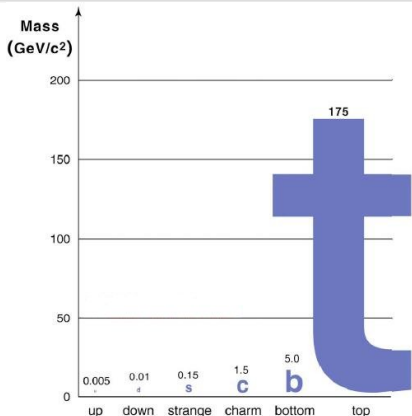
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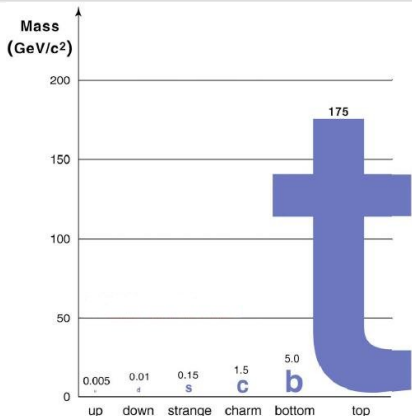
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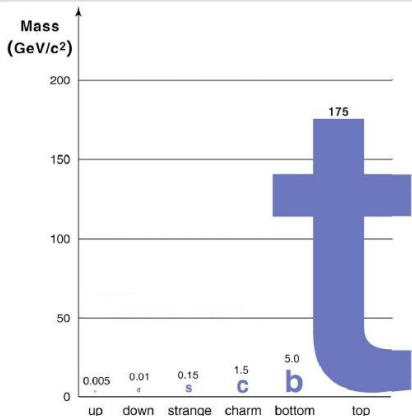
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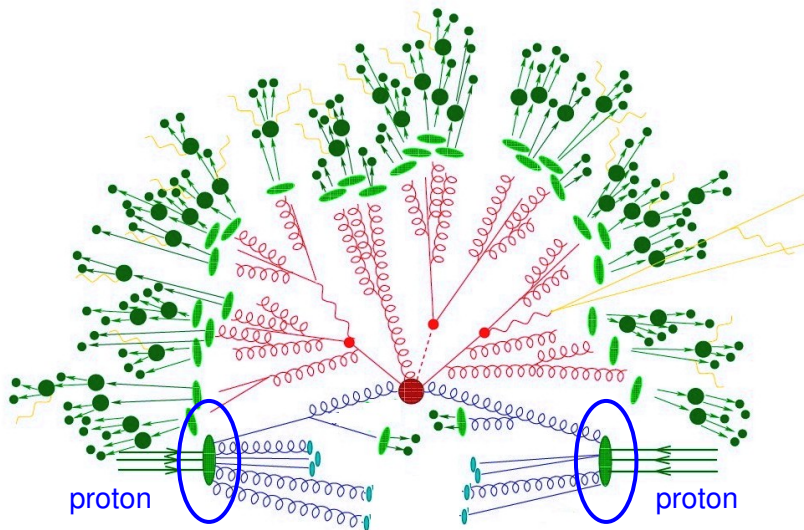
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- **Conclusively**

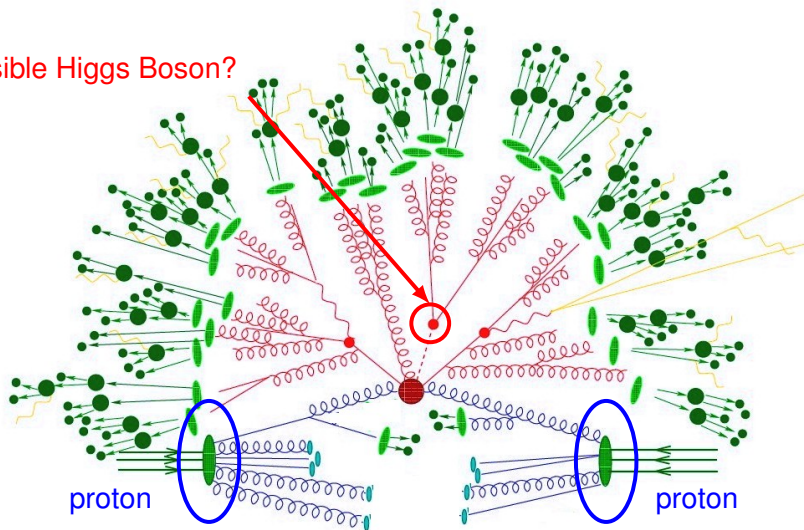


LHC collision process

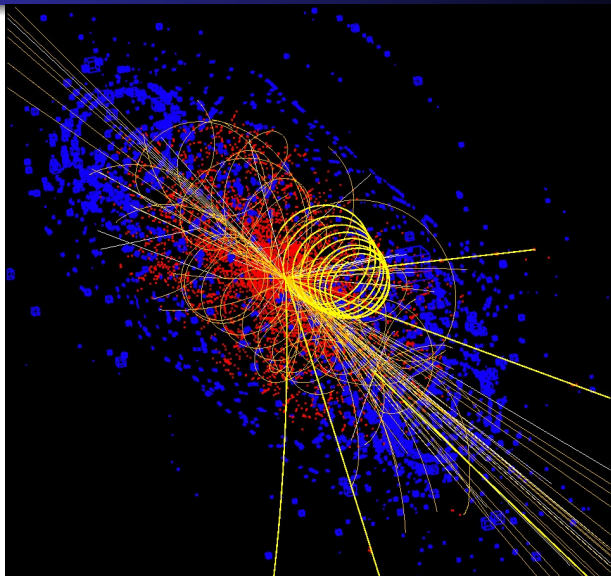


LHC collision process

Possible Higgs Boson?



A simulated Higgs boson event



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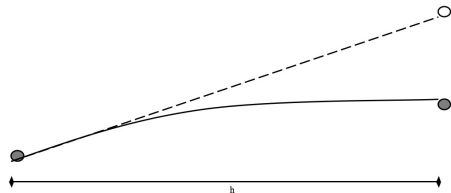
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Preliminary GPGPU test case study

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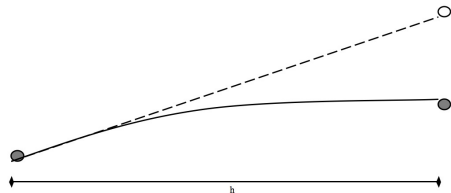


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- Lorentz force (perpendicular to plane of magnetic field)

$$\mathbf{F} = m\mathbf{a} = q \cdot (\mathbf{E} + \mathbf{v} \times \mathbf{B})$$
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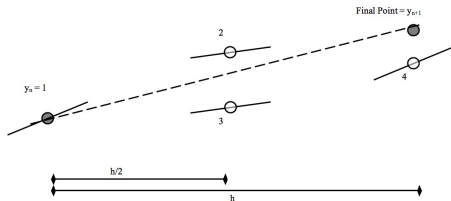
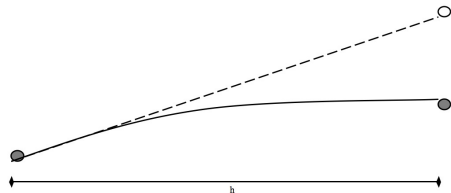
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- Solve the differential equation with 4th order Runge Kutta Integration



Acceleration with GPGPUs

- 1 Using the GPGPU, pre-calculated a “look-up” table of derivative calculations for a space point matrix
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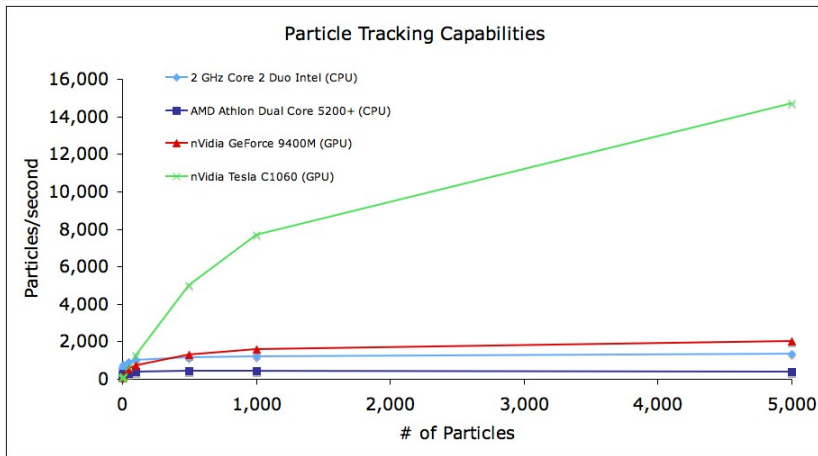
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 - Set up the threads in the block to use shared memory
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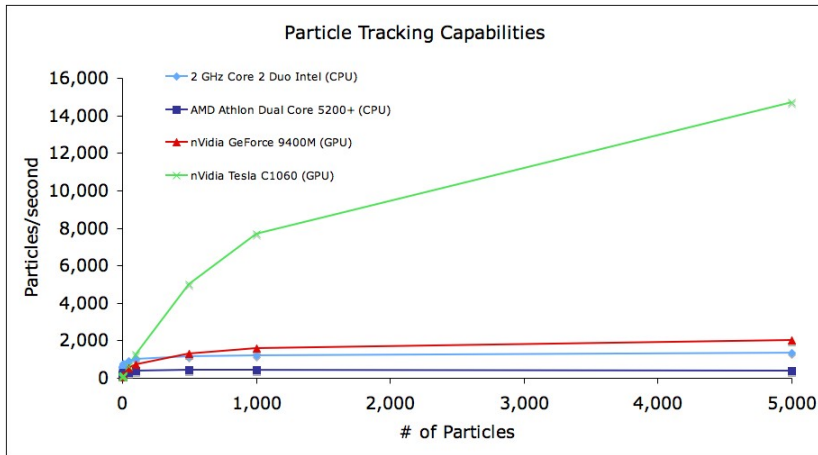
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- ④ Next stage was to do many particle tracks in parallel. . .

Magnetic Field Integration results



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Preliminary results (Tesla C1060)

Rapidly achieved a factor 32 speedup (more in progress)

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ATLAS Trigger and data acquisition

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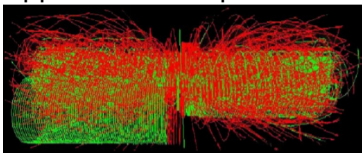
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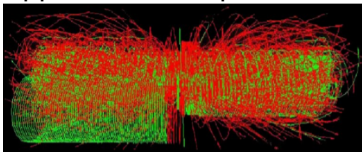
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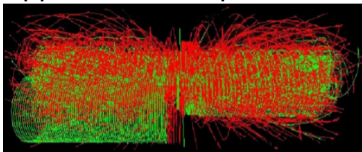
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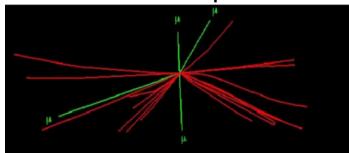
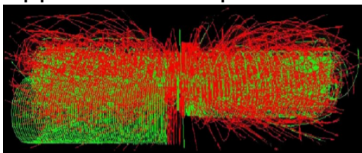
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If we recorded everything it would be Petabytes per second

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The ATLAS Trigger

The solution is to select (**trigger**) events of interest

The ATLAS trigger and software

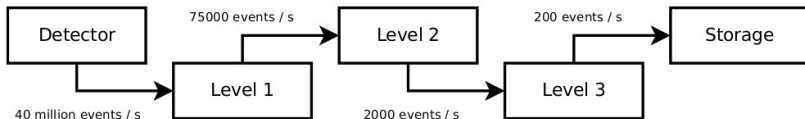
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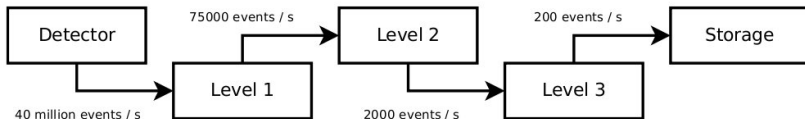
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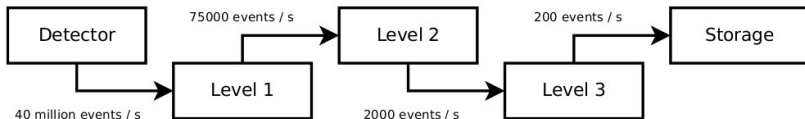


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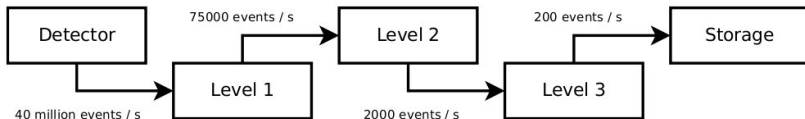
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The software (high-level) trigger farm

- Level 2 and Level 3 triggers collectively called the **high-level trigger (HLT)**



Figure: L2 supervisors, event builder, data logger

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- Around 1000 PCs (XPU: Interchangeable processing unit (i.e. Level 2 or Level 3))



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- For our GPGPU studies we decided to study algorithms that are run in the Level 2 (Z finder and Kalman filter)



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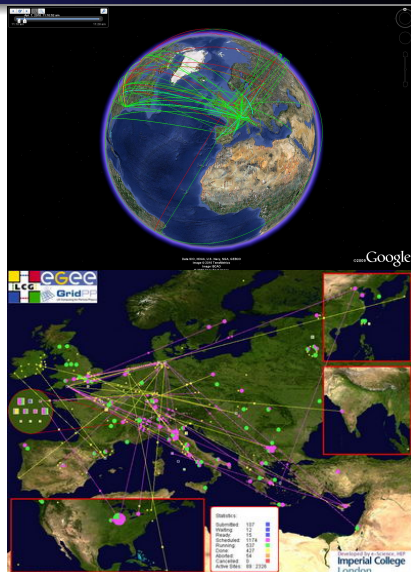
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- Currently >100,000 processors across Grid
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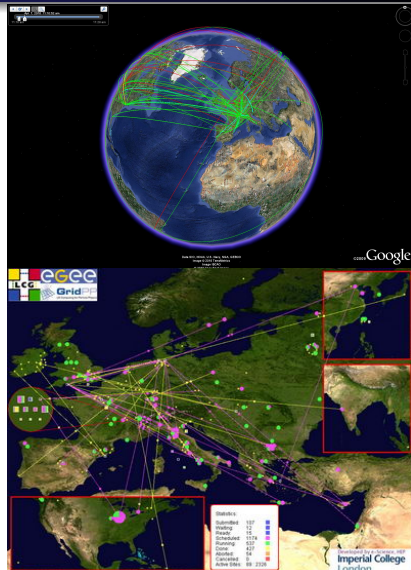


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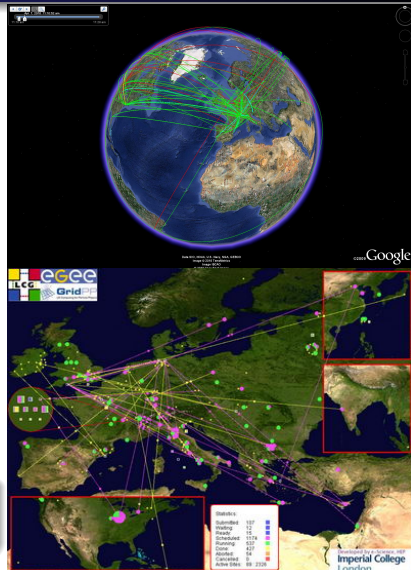
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Up to eight million events simulated daily

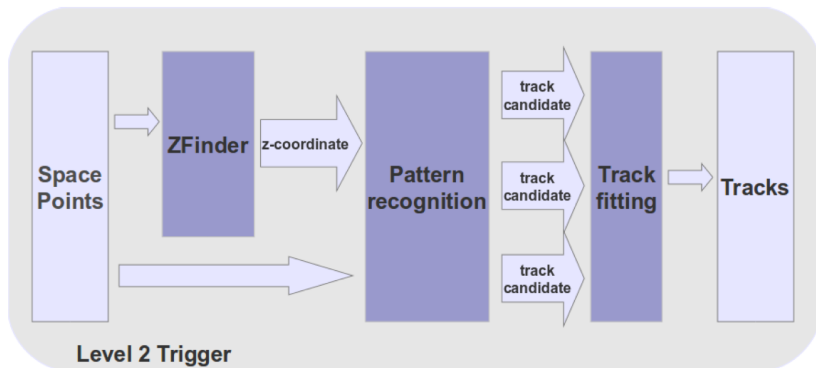
Failure rate is less than 10^{-6}



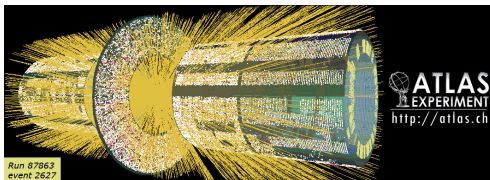
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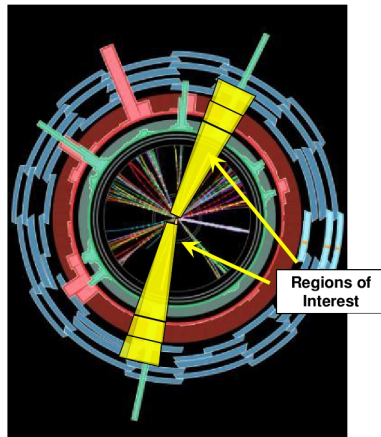
Level 2 Trigger Routines



The Level 2 regions of interest (ROIs)



- We take a cross-section view of the detector
- Break it up into regions of interest (ROIs)
i.e. “phi slices” (ϕ coordinate)
- Candidate for parallelisation using GPUs

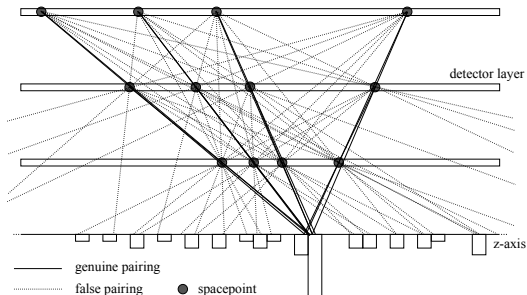


Cross section view of the ATLAS detector

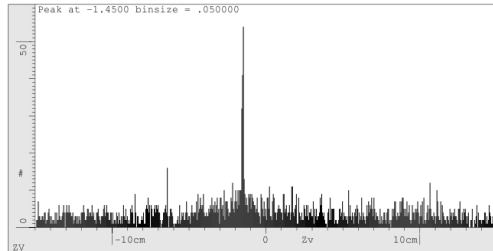
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The Z Finder Algorithm



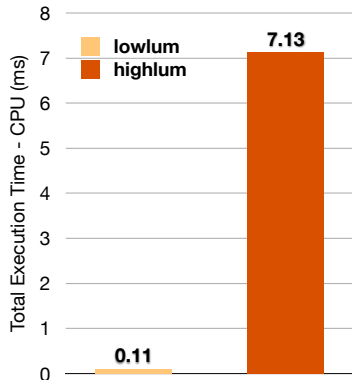
- Process each combination of detector hits ("spacepoints") and extrapolate back to the beam line.
- The histogram peak is the chosen interaction point.



Z Finder Algorithm Test Case

- Z Finder code extracted from ATLAS framework for feasibility studies with CUDA.
- Timing performance measured using two samples of simulated events (low and high *luminosity*).
- Comparison of Tesla and Fermi architectures for each code iteration.

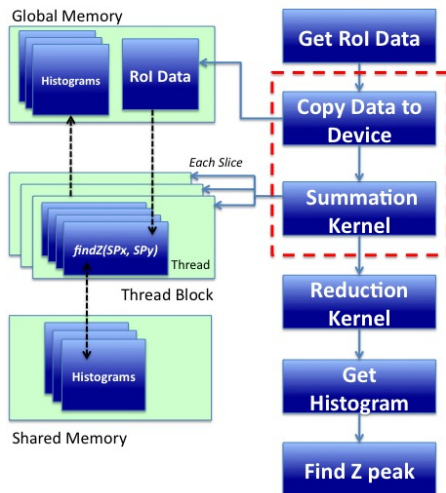
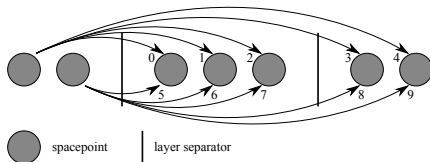
| | lowlum | highlum |
|-------------|--------|---------|
| Spacepoints | 333 | 8104 |



Z Finder Kernel: Histogram Summation

Code Iterations

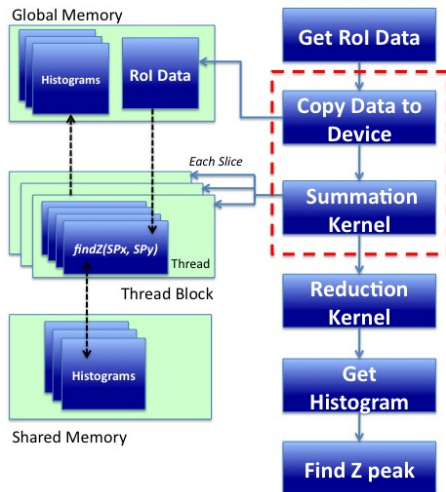
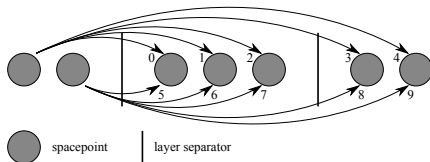
- Single thread per ϕ slice.
- Thread block per ϕ slice.
- Histogram per thread block in shared memory.
- Improve spacepoint pair allocation method.



Z Finder Kernel: Histogram Summation

Code Iterations

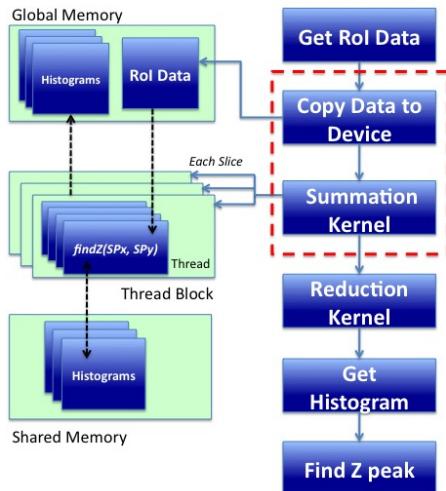
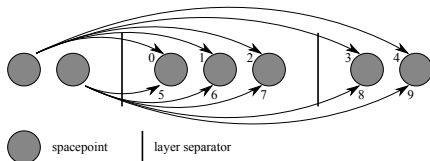
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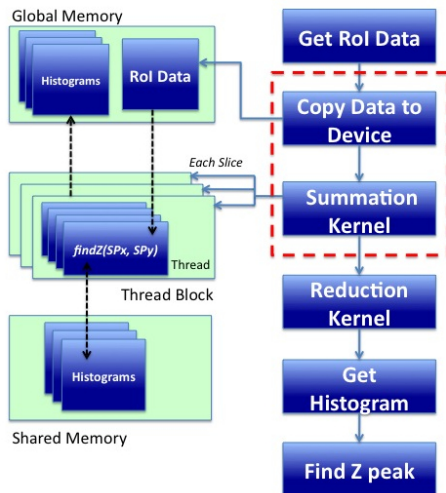
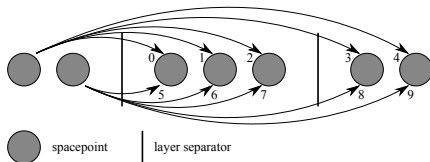
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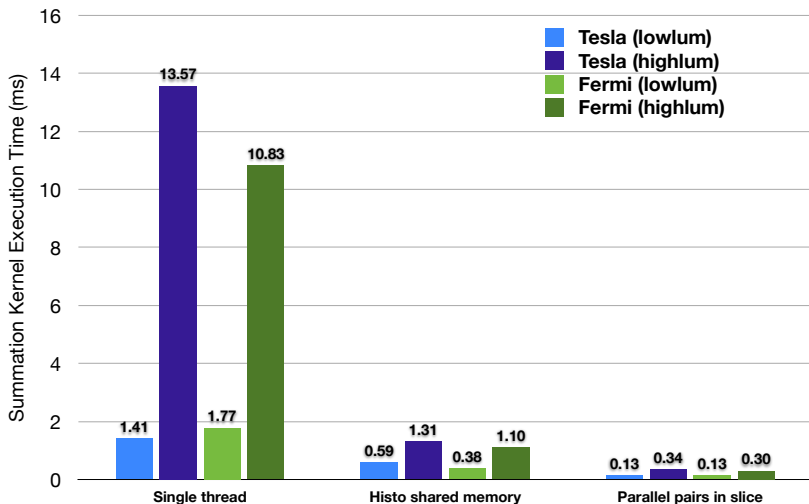
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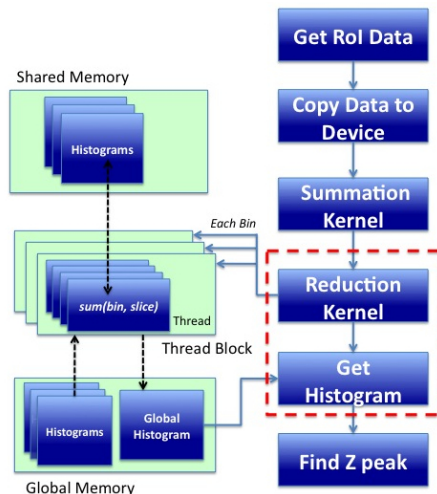
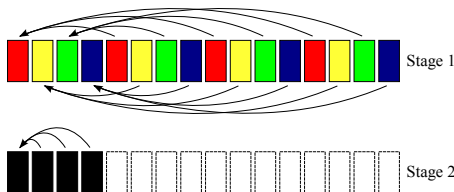
ZFinder Kernel: Histogram Summation Results



ZFinder Kernel: Histogram Combination

Code Iterations

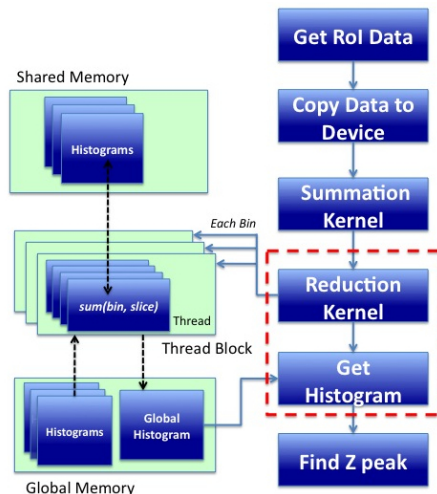
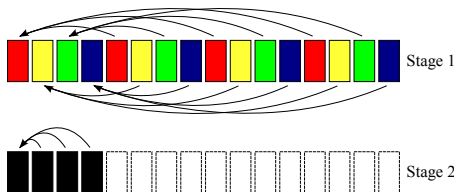
- Combine histograms on the GPU \Rightarrow *reduce data transfer by $\sim 500x$*
- Reduce the data to a single histogram in multiple steps.



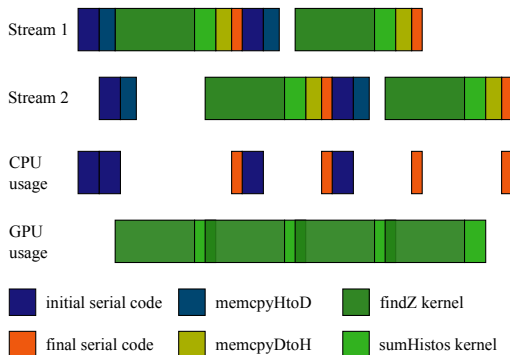
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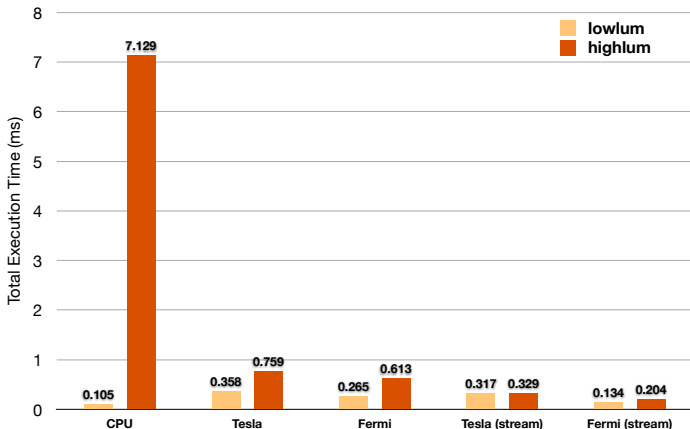


Z Finder Kernel: Streaming



- Each RoI calculation independent \Rightarrow use CUDA streams.
- Successful in disguising any host to device transfer latency.

Z Finder Kernel Results

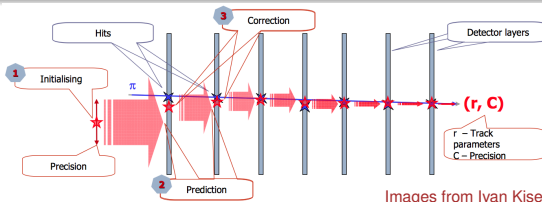


- Initial timing results show up to 35x speed up (Fermi).
- Performance studies continuing with *triplets* of spacepoints.

Outline

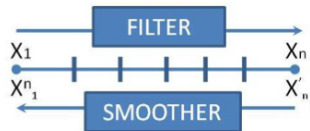
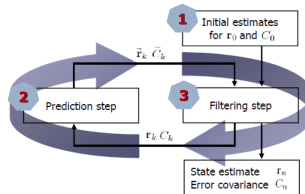
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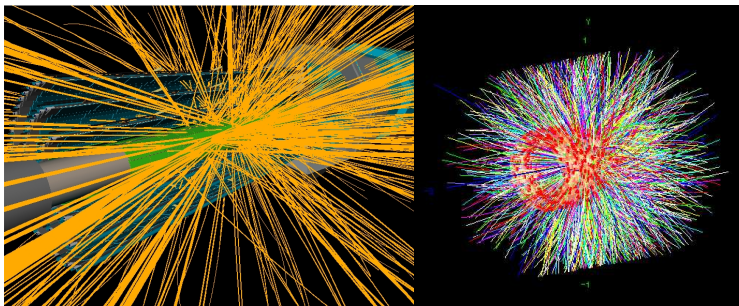


Images from Ivan Kisel, GSI

- Particle tracks reconstructed using the Kalman filter method.
- The trajectory of a track is predicted using detector hits as input.
- A backward smoothing filter is applied after the final Kalman Filter estimation.



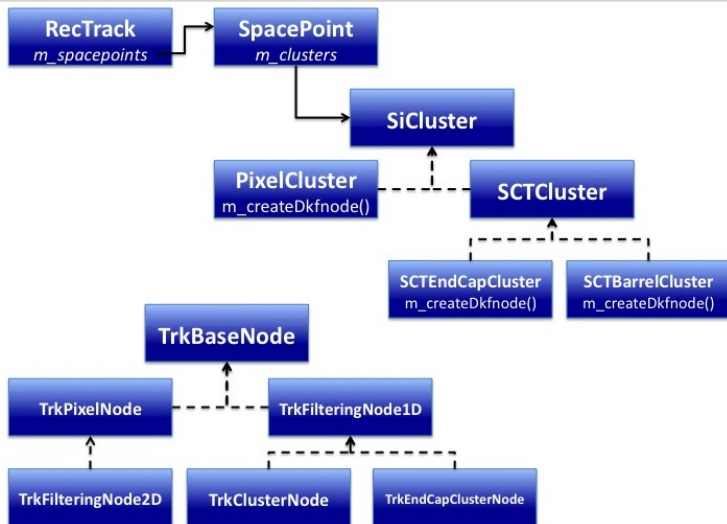
GPU Motivation for Track Reconstruction



ATLAS simulations of high luminosity events

- Potentially *thousands* of tracks to process for every event.
- Significant acceleration possible by reconstructing one track per GPU thread.

ATLAS Kalman Filter Framework



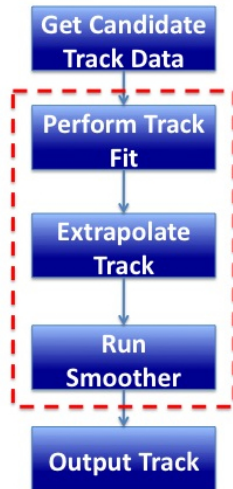
CUDA Challenges

Initial Complications

- Class inheritance structure captures filter specialism for each sub-detector.
- Dynamic creation of objects in the main routine.
- Track state retained at each filtering step.
- Main routine has over 2000+ lines of code with multiple branches.

Feasibility Studies

- Standalone version successfully ported to C.
- Pre-allocated memory needed for track objects.
- Promising results \Rightarrow need to reduce memory usage.



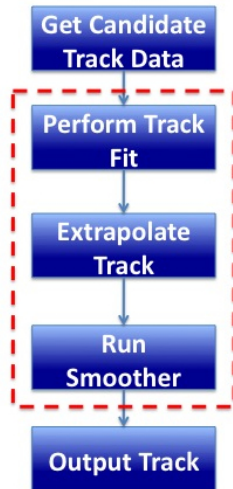
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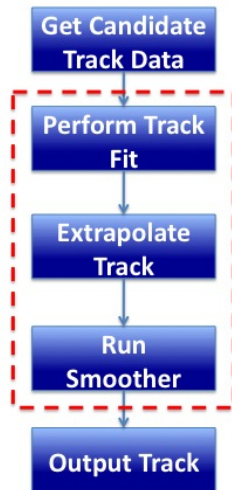
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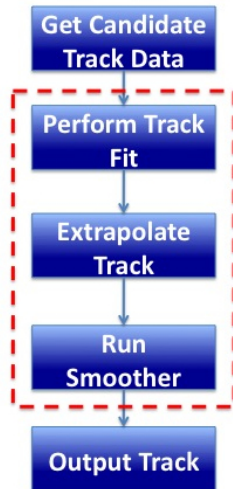
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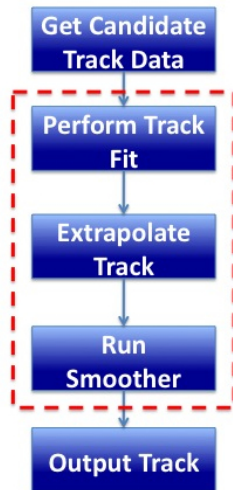
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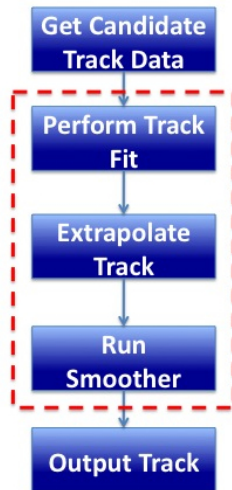
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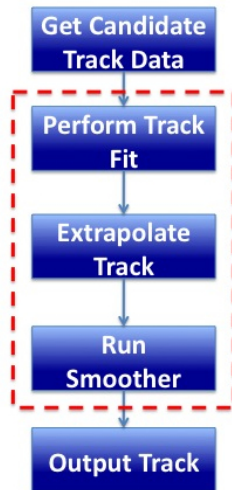
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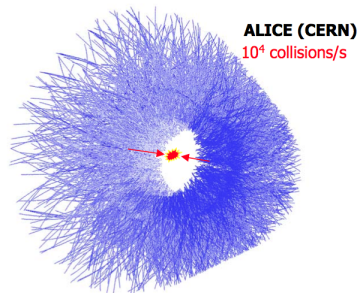
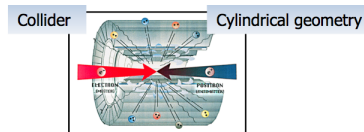


Kalman Filter Potential

- Our present Kalman Filter could be modified.

GPU benefits at other experiments

- Kalman Filter port to CUDA (GSI Scientific Report 2008, FAIR-EXPERIMENTS-38)
- ALICE TPC HLT code GPU based / Future PANDA TPC code
- GPUs to be used for STS (Silicon Tracking System) within CBM (Compressed Baryonic Matter) experiment at FAIR/GSI.



Summary

- The ATLAS trigger, particle tracking & simulation algorithms are key places where GPUs can be used to improve performance.
- Preliminary results show substantial performance.
 - Initial 32x speed-up for parallel RK4 integration.
 - With optimisation up to 35x speed up for Level2 Z Finder.
 - Initial port of OO based Kalman Filter algorithm.

Further information



SIMT design of the High Level Trigger
Kalman Fitter



Porting the Z-finder algorithm to GPU
[ATLAS Edinburgh GPU Computing](#)



LHC and ATLAS papers
[2008 JINST 3 S08003](#)

Thanks to Peter Jenni, Iain Longstaff
for material.

Thanks to NVIDIA for their support