Real-Time Color Space Conversion for High Resolution Video

Color Space Conversion

Color space conversion or color correction is a widely used technique to adapt the color characteristics of video material to the display technology employed (e.g. CRT, LCD, projection) or to create a certain artistic look. As color correction often is an interactive task and colorists need a direct response, state-of-the-art real-time color correction systems for video are so far based on expensive dedicated hardware.



Look-Up-Tables for Color Space Conversion

The use of Look-Up-Tables (LUTs) is a fairly simple, but very powerful approach for the implementation of color space conversion. Depending on the requirements for the conversion, either 1D- or 3D-LUTs have to be deployed.

Using 1D-LUTs, each component of an input pixel addresses one table. This results into a simple implementation with low memory requirements.

Input Pixel (R) •>	1D-LUT	→ Output Pixel (R)	Color Resolution	Memory Space
Input Pixel (G) •	1D-LUT →	→ Output Pixel (G)	8 bit	768 B
			10 bit	3 kB
			12 bit	12 kB
Input Pixel (B) ● →	1D-LUT	→ Output Pixel (B)		

As color correction with 1D-LUTs can only be done for each color component separately, its usage is rather limited to very basic tasks like log-to-lin conversion or brightness control. For more sophisticated color correction tasks, 3D-LUTs have to be used instead. Here, all components of a pixel together build the address for the LUT. Thus, each color of the input color space can be mapped to any color of the output color space.

→ Output Pixel (RGB)

This approach can be applied to almost any color space conversion task, but the memory requirements are extreme.

References: [1] H.-S. Lee, K.-S. Kim, D. Han, "A Real Time Color Gamut Mapping Using Tetrahedral Interpolation for Digital TV Color Reproduction Enhancement", IEEE Transactions on Consumer Electronics, Vol. 55, No. 2, May 2009

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Color Resolution	Memory Space
8 bit	50 MB
10 bit	4 GB
12 bit	309 GB

In order to reduce the memory requirements, a 3D-Reduced-Resolution-Look-Up-Table (3D-RRLUT) is combined with a unit interpolating the output values [1].



This approach reduces the memory requirements significantly. Even for 3D-RRLUTs with 12 bit color resolution and 129 entries for each color component, only 9.7 MB of memory are required.

Color Resolution	Number of Table Entries	Memory Space
8 bit	9x9x9	2.2 kB
	33x33x33	108 kB
	129x129x129	6.4 MB
10 bit	9x9x9	2.7 kB
	33x33x33	135 kB
	129x129x129	8 MB
12 bit	9x9x9	3.3 kB
	33x33x33	162 kB
	129x129x129	9 7 MB

For the additional interpolation step often the efficient and precise Tetrahedral Interpolation is used.

The Tetrahedral Interpolation was implemented on 3 different parallel computing devices (IBM Cell B.E., NVIDIA Geforce GTS250, and NVIDIA Tesla C2050). Parallelization was done on pixel level. To avoid the significant performance loss caused by branches, always all 6 possible interpolations are calculated. The final result is computed with 3 fast select instructions. OpenCL 1.0 was used as programming language for the GPUs. The Cell B.E. was programmed in C incl. Intrinsics.



		interpolation daloulation
T1	f _G ≥f _B ≥f _R	$(1-f_G)\cdot[n_R;n_G;n_B] + (f_G-f_B)\cdot[n_R;n_G+1;n_B] + (f_B-f_R)\cdot[n_R;n_G+1;n_B+1] + (f_R)\cdot[n_R+1;n_G+1;n_B+1]$
T2	f _B >f _R >f _G	$(1-f_B)\cdot[n_R;n_G;n_B] + (f_B-f_R)\cdot[n_R;n_G;n_B+1] + (f_R-f_G)\cdot[n_R+1;n_G;n_B+1] + (f_G)\cdot[n_R+1;n_G+1;n_B+1]$
Т3	f _B >f _G ≥f _R	$(1-f_B)\cdot[n_R;n_G;n_B] + (f_B-f_G)\cdot[n_R;n_G;n_B+1] + (f_G-f_R)\cdot[n_R;n_G+1;n_B+1] + (f_R)\cdot[n_R+1;n_G+1;n_B+1]$
Τ4	f _R ≥f _G >f _B	$(1-f_R)\cdot[n_R;n_G;n_B] + (f_R-f_G)\cdot[n_R+1;n_G;n_B] + (f_G-f_B)\cdot[n_R+1;n_G+1;n_B] + (f_B)\cdot[n_R+1;n_G+1;n_B+1]$
T5	f _G >f _R ≥f _B	$(1-f_G)\cdot[n_R;n_G;n_B] + (f_G-f_R)\cdot[n_R;n_G+1;n_B] + (f_R-f_B)\cdot[n_R+1;n_G+1;n_B] + (f_B)\cdot[n_R+1;n_G+1;n_B+1]$
T6	f _R ≥f _B ≥f _G	$(1-f_R)\cdot[n_R;n_G;n_B] + (f_R-f_B)\cdot[n_R+1;n_G;n_B] + (f_B-f_G)\cdot[n_R+1;n_G;n_B+1] + (f_G)\cdot[n_R+1;n_G+1;n_B+1]$

Implementation



Performance

The computation of the Tetrahedral Interpolation requires 250 arithmetic and logical operations for each pixel. The real-time processing requirements for common video formats range from 2.6 GOPS to 52.4 GOPS.

nage Size	Format	Frame or Field Rate	Operations
720x486	486i60	60 Hz interlaced	2.6 GOPS
1280x720	720p50	50 Hz progressive	11.5GOPS
920x1080	1080p60	60 Hz progressive	31.1 GOPS
048x1536	1536p25	25 Hz progressive	19.7GOPS
096x2048	2048p25	25 Hz progressive	52.4 GOPS

The table below shows the performance of the Tetrahedral Interpolation on the 3 different parallel computing devices. Processing and transfer times are

ng Device	IBM CELL B.E.	Geforce GTS250	Tesla C2050
System	n.a.	Dual Socket XEON 5120	Dual Socket XEON W5590
e Size	1920x1080	1920x1080	1920x1080
ing Time evice	16.6 ms	9 ms	1.5 ms
er Time Device	3.5 ms	4.2 ms	3 ms
er Time to Host	3.5 ms	3.2 ms	3 ms
width Device	2.4 GB/s	2 GB/s	2.7 GB/s
width to Host	2.4 GB/s	2.6 GB/s	2.7 GB/s
ing Time er Time	16.6 ms ¹	16.4ms	7.5 ms
tion Level	Very high	Moderate	Moderate
g Language	C, many intrinsics	OpenCL 1.0	OpenCL 1.0
formance	31.2 GOPS	31.6 GOPS	69.1 GOPS
formance	31.2 GOPS	57.6GOPS	345.6 GOPS
ing Time CPU only	n.a.	820 ms	767 ms
ed-Up CPU only	n.a.	50	102

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on Host

Device vs.

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¹The Cell B.E. System supports parallel data transfer and computation

Conclusion

Ø Tetrahedral Interpolation performs very well on GPUs High performance gain compared to CPUs

High utilization of computational resources on GPU

ØSingle low-cost GTS250 keeps pace with Cell B.E. and is sufficient for real-time color correction of 1080p60

ØSingle high-end Tesla C2050 supports real-time color correction up to the 2048p25 format

GPUs are ready to replace expensive dedicated hardware for real-time color space conversion