N18E-G0 AND N18P-G0-MP GPUs

New Product Notice
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New Product Introduction

NVIDIA™ N18E-G0 AND N18P-G0-MP GPUs

Notification | December 14, 2018 | Planned Implementation | Per schedules below

Product Information

This NPN specifies the new N18E-G0 AND N18P-G0-MP notebook GPUs. N18E-G0 is in the GB4B-256 (2228-ball BGA, 37.5 mm × 37.5 mm) package only. N18P-G0-MP is in the GB4D-128 (960-ball BGA, 29 mm × 29 mm) package only.

Please refer to N18E and N18P design documents and contact your local AE representatives for additional guidance.

This will be manufactured using qualified suppliers per our approved suppliers list.

Key Specifications:

<table>
<thead>
<tr>
<th></th>
<th>N18E-G0</th>
<th>N18P-G0-MP</th>
</tr>
</thead>
<tbody>
<tr>
<td>Product Part Number</td>
<td>N18E-G0-A1</td>
<td>N18P-G0-MP-A1</td>
</tr>
<tr>
<td>NVIDIA Part Number1</td>
<td>TU116-750-A1</td>
<td>TU117-750-A1</td>
</tr>
<tr>
<td>Device ID</td>
<td>• Primary: 0x2191</td>
<td>• Primary: 0x1F91</td>
</tr>
<tr>
<td></td>
<td>• Secondary: 0x21D1</td>
<td>• Secondary: 0x1FD1</td>
</tr>
<tr>
<td>Memory interface</td>
<td>192-bit GDDR6</td>
<td>128-bit GDDR5</td>
</tr>
<tr>
<td>Package</td>
<td>GB4B-256</td>
<td>GB4D-128</td>
</tr>
</tbody>
</table>

Impact of Change and Recommended Action

NVIDIA is committed to providing our customers with quality products that push the edge of technology and at the same time enable product segmentation. To help guarantee a rapid time-to-market launch and smooth and uninterrupted product supply, NVIDIA strongly recommends that customers qualify N18E-G0 and N18P-G0-MP GPUs as soon as possible.

Forecasted Key Milestones

<table>
<thead>
<tr>
<th>N18E-G0-A1: PS/MP</th>
<th>January 2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>N18P-G0-MP-A1: PS/MP</td>
<td>January 2019</td>
</tr>
</tbody>
</table>
Product Marking and Traceability

N18E-G0-A1 and N18P-G0-MP-A1 Markings

- **Pin 1:** Location of Pin 1 in upper left when reading the marking.
- **Line 1:** Company Name
- **Line 2:** Vendor ID (C)
  - Country of origin (COO)
  - A = ASE
  - B = Amkor K5
  - K = Amkor K4
  - R = Amkor
  - S = SPI
  - T = TSMC
  - Country of origin = Taiwan for ASE, Amkor, SPI, and TSMC, and Korea for Amkor K4 and Amkor K5
- **YYWW:** Assembly date code
- **AB:** Die revision
- **Line 3:** Lot number (XXXXXXXX)
  - (Assembly lot number from ABR)
- **Line 4:** Product Part Number
- **Line 5:** Qual/Eng Sample
  - Line is blank for PS and MP

Products Affected/Ordering Codes

<table>
<thead>
<tr>
<th>Product</th>
<th>Marketing Part Number for P.O.</th>
<th>NVIDIA Part Number¹</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>N18P-G0-MP-A1</td>
<td>N18P-G0-MP-A1</td>
<td>TU117-750-A1</td>
<td>Optional GDDR5 kit is available on this SKU. See GDDR5 Graphics Memory Bundle NPN for more details.</td>
</tr>
</tbody>
</table>

**Note:**
¹. The NVIDIA Part Number is provided in this document as a reference product shipping and handling at factory. This is NVIDIA confidential.
## Revision History

<table>
<thead>
<tr>
<th>Version</th>
<th>Date</th>
<th>Authors</th>
<th>Description of Change</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>December 14, 2018</td>
<td>MA, KF, DV</td>
<td>Initial release</td>
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</table>
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