NVIDIA H100 PCIe GPU

Product Brief
## Document History

<table>
<thead>
<tr>
<th>Version</th>
<th>Date</th>
<th>Authors</th>
<th>Description of Change</th>
</tr>
</thead>
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Overview

The NVIDIA® H100 Tensor Core GPU delivers unprecedented acceleration to power the world’s highest-performing elastic data centers for AI, data analytics, and high-performance computing (HPC) applications. NVIDIA H100 Tensor Core technology supports a broad range of math precisions, providing a single accelerator for every compute workload. The NVIDIA H100 PCIe supports double precision (FP64), single-precision (FP32), half precision (FP16), and integer (INT8) compute tasks.

NVIDIA H100 Tensor Core graphics processing units (GPUs) for mainstream servers comes with an NVIDIA AI Enterprise five-year software subscription and includes enterprise support, simplifying AI adoption with the highest performance. This ensures organizations have access to the AI frameworks and tools needed to build H100 accelerated AI workflows such as conversational AI, recommendation engines, vision AI, and more.

Activate NVIDIA AI Enterprise license for H100 at: https://www.nvidia.com/activate-h100/

The NVIDIA H100 card is a dual-slot 10.5 inch PCI Express Gen5 card based on the NVIDIA Hopper™ architecture. It uses a passive heat sink for cooling, which requires system airflow to operate the card properly within its thermal limits. The NVIDIA H100 PCIe operates unconstrained up to its maximum thermal design power (TDP) level of 350 W to accelerate applications that require the fastest computational speed and highest data throughput. The NVIDIA H100 PCIe debuts the world’s highest PCIe card memory bandwidth greater than 2,000 gigabytes per second (GBps). This speeds time to solution for the largest models and most massive data sets.

The NVIDIA H100 PCIe card features Multi-Instance GPU (MIG) capability. This can be used to partition the GPU into as many as seven hardware-isolated GPU instances, providing a unified platform that enables elastic data centers to adjust dynamically to shifting workload demands. As well as one can allocate the right size of resources from the smallest to biggest multi-GPU jobs. NVIDIA H100 versatility means that IT managers can maximize the utility of every GPU in their data center.

NVIDIA H100 PCIe cards use three NVIDIA® NVLink® bridges. They are the same as the bridges used with NVIDIA A100 PCIe cards. This allows two NVIDIA H100 PCIe cards to be connected to deliver 900 GB/s bidirectional bandwidth or 5x the bandwidth of PCIe Gen5, to maximize application performance for large workloads.

The list of qualified H100 servers is TBD.
Figure 1. NVIDIA H100 with NVLink Bridge Volumetric
Specifications

Product Specifications

Table 1 through Table 3 the product, memory, and software specifications for the NVIDIA H100 PCIe card.

Table 1. Product Specifications

<table>
<thead>
<tr>
<th>Specification</th>
<th>NVIDIA H100</th>
</tr>
</thead>
<tbody>
<tr>
<td>Product SKU</td>
<td>P1010 SKU 200</td>
</tr>
<tr>
<td></td>
<td>NVPN: 699-21010-0200-xxx</td>
</tr>
<tr>
<td>Total board power</td>
<td>PCIe 16-pin 450 W or 600 W power mode:</td>
</tr>
<tr>
<td></td>
<td>• 350 W default</td>
</tr>
<tr>
<td></td>
<td>• 350 W maximum</td>
</tr>
<tr>
<td></td>
<td>• 200 W minimum</td>
</tr>
<tr>
<td></td>
<td>PCIe 16-pin 300 W power mode:</td>
</tr>
<tr>
<td></td>
<td>• 310 W default</td>
</tr>
<tr>
<td></td>
<td>• 310 W maximum</td>
</tr>
<tr>
<td></td>
<td>• 200 W minimum</td>
</tr>
<tr>
<td>Thermal solution</td>
<td>Passive</td>
</tr>
<tr>
<td>Mechanical form factor</td>
<td>Full-height, full-length [FHFL] 10.5&quot;, dual-slot</td>
</tr>
<tr>
<td>GPU SKU</td>
<td>GH100-200</td>
</tr>
<tr>
<td>PCI Device IDs</td>
<td>Device ID: 0x2331</td>
</tr>
<tr>
<td></td>
<td>Vendor ID: 0x10DE</td>
</tr>
<tr>
<td></td>
<td>Sub-Vendor ID: 0x10DE</td>
</tr>
<tr>
<td></td>
<td>Sub-System ID: 0x1626</td>
</tr>
<tr>
<td>GPU clocks</td>
<td>Base: 1,125 MHz</td>
</tr>
<tr>
<td></td>
<td>Boost: 1,755 MHz</td>
</tr>
<tr>
<td>Performance states</td>
<td>P0</td>
</tr>
<tr>
<td>VBIOS</td>
<td>EEPROM size: 8 Mbit</td>
</tr>
<tr>
<td></td>
<td>UEFI: Supported</td>
</tr>
</tbody>
</table>
## Specifications

<table>
<thead>
<tr>
<th>Specification</th>
<th>NVIDIA H100</th>
</tr>
</thead>
<tbody>
<tr>
<td>PCI Express interface</td>
<td>PCI Express Gen5 x16; Gen5 x8; Gen4 x16</td>
</tr>
<tr>
<td></td>
<td>Lane and polarity reversal supported</td>
</tr>
<tr>
<td>Multi-Instance GPU (MIG)</td>
<td>Supported [seven instances]</td>
</tr>
<tr>
<td>Secure Boot (CEC)</td>
<td>Supported</td>
</tr>
<tr>
<td>Zero Power</td>
<td>Not supported</td>
</tr>
<tr>
<td>Power connectors and headers</td>
<td>One PCIe 16-pin auxiliary power connector</td>
</tr>
<tr>
<td>Weight</td>
<td>Board: 1200g grams [excluding bracket, extenders, and bridges]</td>
</tr>
<tr>
<td></td>
<td>NVLink bridge: 20.5 grams per bridge [x 3 bridges]</td>
</tr>
<tr>
<td></td>
<td>Bracket with screws: 20 grams</td>
</tr>
<tr>
<td></td>
<td>Enhanced straight extender: 35 grams</td>
</tr>
<tr>
<td></td>
<td>Long offset extender: 48 grams</td>
</tr>
<tr>
<td></td>
<td>Straight extender: 32 grams</td>
</tr>
</tbody>
</table>

### Table 2. Memory Specifications

<table>
<thead>
<tr>
<th>Specification</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Memory clock</td>
<td>1,593 MHz</td>
</tr>
<tr>
<td>Memory type</td>
<td>HBM2e</td>
</tr>
<tr>
<td>Memory size</td>
<td>80 GB</td>
</tr>
<tr>
<td>Memory bus width</td>
<td>5,120 bits</td>
</tr>
<tr>
<td>Peak memory bandwidth</td>
<td>2,000 GB/s</td>
</tr>
</tbody>
</table>

### Table 3. Software Specifications

<table>
<thead>
<tr>
<th>Specification</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SR-IOV support</td>
<td>Supported -- 32 VF [virtual functions]</td>
</tr>
<tr>
<td>BAR address (physical function)</td>
<td>BAR0: 16 MiB&lt;sup&gt;1&lt;/sup&gt;</td>
</tr>
<tr>
<td></td>
<td>BAR1: 128 GiB&lt;sup&gt;1&lt;/sup&gt;</td>
</tr>
<tr>
<td></td>
<td>BAR3: 32 MiB&lt;sup&gt;1&lt;/sup&gt;</td>
</tr>
<tr>
<td>BAR address (virtual function)</td>
<td>BAR0: 5 MiB, [256 KiB per VF]&lt;sup&gt;1&lt;/sup&gt;</td>
</tr>
<tr>
<td></td>
<td>BAR1: 80 GiB, 64-bit [4 GiB per VF]&lt;sup&gt;1&lt;/sup&gt;</td>
</tr>
<tr>
<td></td>
<td>BAR3: 640 MiB, 64-bit [32 MiB per VF]&lt;sup&gt;1&lt;/sup&gt;</td>
</tr>
<tr>
<td>Message signaled interrupts</td>
<td>MSI-X: Supported</td>
</tr>
<tr>
<td></td>
<td>MSI: Not supported</td>
</tr>
<tr>
<td>ARI Forwarding</td>
<td>Supported</td>
</tr>
<tr>
<td>Driver support</td>
<td>Linux: R520 or later</td>
</tr>
<tr>
<td></td>
<td>Windows: R520 or later</td>
</tr>
<tr>
<td>Specification</td>
<td>Description</td>
</tr>
<tr>
<td>--------------------------------</td>
<td>---------------------------------------------------</td>
</tr>
<tr>
<td>Secure Boot</td>
<td>Supported</td>
</tr>
<tr>
<td>CEC Firmware</td>
<td>Version 2.0025 or later</td>
</tr>
<tr>
<td>NVFlash</td>
<td>Version 5.792 or later</td>
</tr>
<tr>
<td>NVIDIA® CUDA® support</td>
<td>x86: CUDA 11.8 or later</td>
</tr>
<tr>
<td></td>
<td>Arm: CUDA 12.0 or later</td>
</tr>
<tr>
<td>Virtual GPU software support</td>
<td>Supports vGPU 15.0 or later: NVIDIA Virtual Compute</td>
</tr>
<tr>
<td></td>
<td>Server Edition</td>
</tr>
<tr>
<td>NVIDIA AI Enterprise</td>
<td>Supported with VMWare</td>
</tr>
<tr>
<td>NVIDIA certification</td>
<td>NVIDIA-Certified Systems™ TBD or later</td>
</tr>
<tr>
<td>PCI class code</td>
<td>0x03 – Display Controller</td>
</tr>
<tr>
<td>PCI subclass code</td>
<td>0x02 – 3D Controller</td>
</tr>
<tr>
<td>ECC support</td>
<td>Enabled</td>
</tr>
<tr>
<td>SMBus (8-bit address)</td>
<td>0x9E (write), 0x9F (read)</td>
</tr>
<tr>
<td>IPMI FRU EEPROM I2C address</td>
<td>0x50 (7-bit), 0xA0 (8-bit)</td>
</tr>
<tr>
<td>Reserved I2C addresses</td>
<td>0xAA, 0xAC</td>
</tr>
<tr>
<td>SMBus direct access</td>
<td>Supported</td>
</tr>
<tr>
<td>SMBPBI</td>
<td>Supported</td>
</tr>
</tbody>
</table>

Note:  
1The KiB, MiB, and GiB notation emphasize the “power of two” nature of the values. Thus, 
- 256 KiB = 256 x 1024
- 16 MiB = 16 x 1024²
- 64 GiB = 64 x 1024³

Thermal Specifications
Table 4 provides the PCIe reported temperatures and Table 5 provides the thermal specifications for the NVIDIA H100 PCIe card.

Table 4. H100 PCIe Reported Temperatures

<table>
<thead>
<tr>
<th>Specification</th>
<th>Units</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>$T_{AVG}$</td>
<td>ºC</td>
<td>Average temperature of all internal GPU sensors</td>
</tr>
<tr>
<td>$T_{LIMIT}$</td>
<td>ºC</td>
<td>GPU and HBM temperature limit – current distance in degrees C from software slowdown event</td>
</tr>
<tr>
<td>$T_{HBM}$</td>
<td>ºC</td>
<td>Maximum temperature of all HBM sensors</td>
</tr>
</tbody>
</table>
### Table 5. Thermal Specifications

<table>
<thead>
<tr>
<th>Specification</th>
<th>Applies to</th>
<th>Thermal Parameter Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Thermal qualification temperature</td>
<td>GPU</td>
<td>$T_{\text{AVG}} = 87^\circ\text{C}$</td>
</tr>
<tr>
<td></td>
<td>HBM</td>
<td>$T_{\text{HBM}} = 95^\circ\text{C}$</td>
</tr>
<tr>
<td>Maximum operating temperature</td>
<td>GPU</td>
<td>$T_{\text{LIMIT}} = 0^\circ\text{C}$</td>
</tr>
<tr>
<td>Hardware slowdown temperature (50% clock slowdown)</td>
<td>GPU</td>
<td>$T_{\text{LIMIT}} = -2^\circ\text{C}$</td>
</tr>
<tr>
<td>Hardware shutdown temperature</td>
<td>GPU</td>
<td>$T_{\text{LIMIT}} = -5^\circ\text{C}$</td>
</tr>
</tbody>
</table>
Airflow Direction Support

The NVIDIA H100 PCIe card employs a bidirectional heat sink, which accepts airflow either left-to-right or right-to-left directions.

Figure 2. NVIDIA H100 Airflow Directions
Product Features

Form Factor
The NVIDIA H100 PCIe card conforms to NVIDIA Form Factor 5.5 specification for a full-height, full-length (FHFL) dual-slot PCIe card. For details refer to the NVIDIA Form Factor 5.5 Specification for Enterprise PCIe Products Specification (NVOnline: 1063377).

Figure 3. NVIDIA H100 PCIe Card Dimensions

NVLink Bridge Support
NVIDIA NVLink is a high-speed point-to-point (P2P) peer transfer connection. Where one GPU can transfer data to and receive data from one other GPU. The NVIDIA H100 card supports NVLink bridge connection with a single adjacent NVIDIA H100 card.
Each of the three attached bridges spans two PCIe slots. To function correctly as well as to provide peak bridge bandwidth, bridge connection with an adjacent NVIDIA H100 card must incorporate all three NVLink bridges. Wherever an adjacent pair of NVIDIA H100 cards exists in
the server, for best bridging performance and balanced bridge topology, the NVIDIA H100 pair should be bridged. Figure 4 illustrates correct and incorrect NVIDIA H100 NVLink connection topologies.

Figure 4. NVLink Topology – Top View

For systems that feature multiple CPUs, both NVIDIA H100 cards of a bridged card pair, should be within the same CPU domain. That is, under the same CPU’s topology, and ensuring this benefits workload application performance. There are exceptions, for example, in a system with dual CPUs wherein each CPU has a single NVIDIA H100 PCIe card under it. In that case, the two NVIDIA H100 PCIe cards in the system may be bridged together. See Section “PCIe and NVLink Topology.”

NVIDIA H100 PCIe card, NVLink speed, and bandwidth are given in the following table.

Table 6. H100 PCIe Card NVLink Speed and Bandwidth

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Total NVLink bridges supported by NVIDIA H100</td>
<td>3</td>
</tr>
<tr>
<td>Total NVLink Rx and Tx lanes supported</td>
<td>48</td>
</tr>
<tr>
<td>Data rate per NVIDIA H100 NVLink lane [each direction]</td>
<td>100 Gbps</td>
</tr>
<tr>
<td>Total maximum NVLink bandwidth</td>
<td>600 Gbytes per second</td>
</tr>
</tbody>
</table>

NVLink Bridge

The 2-slot NVLink bridge for the NVIDIA H100 PCIe card (the same NVLink bridge used in the NVIDIA Ampere Architecture generation, including the NVIDIA A100 PCIe card), has the following NVIDIA part number: 900-53651-0000-000.

NVLink Connector Placement

Figure 5 shows the connector keepout area for the NVLink bridge support of the NVIDIA H100.
Sufficient clearance must be provided both above the card’s north edge and behind the backside of the card’s PCB to accommodate NVIDIA H100 NVLink bridges. The clearance above the north edge should meet or exceed 2.5 mm. The backside clearance (from the rear card’s rear PCB surface) should meet or exceed 2.67 mm. Consult NVIDIA Form Factor 5.5 Specification for Enterprise PCIe Products Specification [NVOnline: 1063377] for more detailed information.

NVLink bridge interfaces of the H100 PCIe card include removable caps to protect the interfaces in non-bridged system configurations.

### PCIe and NVLink Topology

As stated, it is strongly recommended that both NVIDIA H100 PCIe cards of a bridged card pair should be within the same CPU topology domain. Unless a dual CPU system has only two H100 PCIe cards each of which is under its own CPU. Full NVLink connection topology guidance is as follows:

- **Best NVLink Topology (Recommended):**
  - Bridge two GPUs under the same CPU or PCIe switch
  - GPU count in a system should be in powers of two (1, 2, 4, 8, and so on)
  - Locate the same (even) number of GPUs under each CPU socket
  - Maintain a balanced configuration: same count of CPU:GPU:NIC for each grouping

- **Good NVLink Topology:**
  - Bridge two GPUs under different PCIe switches but under the same CPU
  - Same number of GPUs and NICs under each CPU socket, but not powers of 2

- **Allowed but Not Recommended:**
  - Bridge two GPUs under two different CPUs
  - Odd number of GPUs under each CPU
  - Unbalanced configurations: Different ratios of CPU:GPU:NIC for each grouping
Power Connector

This section details the power connector for the NVIDIA H100 PCIe card.

Power Connector Placement

The board provides a PCIe 16-pin power connector on the east edge of the board.

Figure 6. PCIe 16-Pin Power Connector

Table 7 lists the power level options identifiable by the PCIe 16-pin power connector per CEM5 PSU, and the corresponding Sense0 and Sense1 logic. The NVIDIA card senses the Sense0 and Sense1 levels and recognizes the power available to the NVIDIA card from the power connector. If the power level identified by Sense0 and Sense1 is equal to or greater than what the NVIDIA card needs from the 16-pin connector, the NVIDIA card operates per normal. If the power level identified by Sense0 and Sense1 is less than the default power cap of the NVIDIA card, the card will not boot.

The NVIDIA H100 requires up to 350 W from the 16-pin auxiliary power connector. Table 7 shows the supported auxiliary power connector sense pin logic and maximum supported TGP per power level.
Table 7. PCIe CEM 5.0 16-Pin PCIe PSU Power Level vs. Sense Logic

<table>
<thead>
<tr>
<th>Power Level</th>
<th>Sideband 3 [Sense0]</th>
<th>Sideband 4 [Sense1]</th>
<th>Maximum TGP</th>
</tr>
</thead>
<tbody>
<tr>
<td>451 - 600 W</td>
<td>0</td>
<td>0</td>
<td>350 W</td>
</tr>
<tr>
<td>301 - 450 W</td>
<td>1 (float)</td>
<td>0</td>
<td>350 W</td>
</tr>
<tr>
<td>151 - 300 W</td>
<td>0</td>
<td>1 (float)</td>
<td>310 W</td>
</tr>
<tr>
<td>Up to 150 W</td>
<td>1 (float)</td>
<td>1 (float)</td>
<td>Not supported. Insufficient power</td>
</tr>
</tbody>
</table>

Table 8 lists supported auxiliary power connections for the NVIDIA H100 GPU card.

Table 8. Supported Auxiliary Power Connections

<table>
<thead>
<tr>
<th>Board Connector</th>
<th>PSU Cable</th>
</tr>
</thead>
<tbody>
<tr>
<td>PCIe 16-pin</td>
<td>PCIe 16-pin</td>
</tr>
<tr>
<td>PCIe 16-pin</td>
<td>CPU 8-pin to PCIe 16-pin</td>
</tr>
</tbody>
</table>

**CPU 8-Pin to PCIe 16-Pin Power Adapter**

A CPU 8-pin to PCIe 16-pin power adapter is available for systems that do not have native PCIe 16-pin power connectors. Figure 7 illustrates the power adapter. The power adapter provided by NVIDIA can only support 310 W TGP operation. Partners are advised to build their own power adapters (if necessary) to support the 301 W-450 W power sense option to enable full 350 W TGP operation of the H100 PCIe card.

- NVPN: 030-1546-000 – CPU 8-pin to PCIe 16-Pin Power Adapter
- Astron MFN: DAMAF01041-H

**Figure 7. CPU 8-Pin to PCIe 16-Pin Power Adapter**
Figure 8 shows the CPU 8-pin to PCIe 16-pin power adapter pin assignments.

**Figure 8. CPU 8-Pin to PCIe 16-Pin Power Adapter Pin Assignments**

<table>
<thead>
<tr>
<th>PIN ASSIGNMENT</th>
</tr>
</thead>
<tbody>
<tr>
<td>P1</td>
</tr>
<tr>
<td>1</td>
</tr>
<tr>
<td>2</td>
</tr>
<tr>
<td>3</td>
</tr>
<tr>
<td>4</td>
</tr>
<tr>
<td>5</td>
</tr>
<tr>
<td>6</td>
</tr>
<tr>
<td>7</td>
</tr>
<tr>
<td>8</td>
</tr>
<tr>
<td>9</td>
</tr>
<tr>
<td>10</td>
</tr>
<tr>
<td>11</td>
</tr>
<tr>
<td>12</td>
</tr>
<tr>
<td>S3</td>
</tr>
<tr>
<td>S2</td>
</tr>
<tr>
<td>S4</td>
</tr>
</tbody>
</table>

**Note:** The power adapter supports the four Sideband signals, hardware-strapped per Row 3. This strapping corresponds to the "151 – 300 W" power level PCIe CEM 5.0 specification (shown in Row 3). As a result, it supports only a 310 W TGP for the H100 PCIe card. To support a TGP of 350 W, a power adapter or cable strapped as in Row 1 or Row 2 should be used.

**Power Adapter Availability**

The power adapter is provided with sample NVIDIA H100 PCIe cards only. For production cards, consult NVIDIA applications engineering for qualified suppliers of a power adapter.
Extenders

The NVIDIA H100 PCIe card provides two extender options, shown in Figure 9 and Figure 10.

- NVVPN: 151-0398-000 -- Enhanced Straight Extender
  - Card + extender = 312 mm
- NVVPN: 682-00007-5555-001 – Straight extender
  - Card + extender = 312 mm
- NVVPN: 682-00007-5555-000 – Long offset extender
  - Card + extender = 339 mm

Using a standard NVIDIA extender ensures greatest forward compatibility with future NVIDIA product offerings.

If the standard extender will not work, OEMs may design a custom attach method using the extender-mounting holes on the east edge of the PCIe card.

Figure 9. Enhanced Straight Extender

Figure 10. Legacy Long Offset and Straight Extenders
NVIDIA AI Enterprise Software Suite

H100 for mainstream servers comes with a five-year subscription. It includes enterprise support to the NVIDIA AI Enterprise software suite and simplifying AI adoption with the highest performance. This ensures organizations have access to the AI frameworks and tools they must build H100-accelerated AI workflows such as AI chatbots, recommendation engines, vision AI, and more.

Customers can activate their licenses at: https://www.nvidia.com/activate-h100/

The OS of the NVIDIA AI platform, NVIDIA AI Enterprise is essential for production and support of applications built with the extensive NVIDIA library of frameworks and pre-trained models such as NVIDIA® Riva for speech AI, NVIDIA Merlin™ for recommendation engines, NVIDIA Clara™ for medical imaging and more. Certified to deploy NVIDIA-Certified Systems from leading server vendors.

Optimize every step of the AI workflow including data prep, model training, inference, and deployment at scale with NVIDIA AI tools and frameworks.

- Accelerate Data Prep with NVIDIA RAPIDS™
- Train at Scale with the NVIDIA TAO Toolkit
- Optimized for Inference NVIDIA® TensorRT™
- Deploy to Scale NVIDIA Triton™ Inference Server

A broad ecosystem of certified partner integrations reduces deployment risk.

- MLOps solution providers for collaboration and productivity,
- VMware vSphere, VMware Cloud Foundation and VMware Cloud Director to scale in virtualized environments
- Red Hat OpenShift certification
Support for Production AI

Organizations get the transparency of open source with the assurance of support from NVIDIA when they move from development to production.

- Full enterprise support for their AI workflows.
  - Conversational AI
  - Recommendation engines
  - Medical imaging and more.
- Access to NVIDIA AI experts and engineering teams.
- Priority notifications related to the latest security fixes and maintenance releases.
- Enterprise Training Services included so developers, data scientists, and IT professionals get the most out of NVIDIA AI Enterprise.
- Long-term support (LTS) for up to three-years for designated software branches.
- Customized support upgrade options, including a designated technical account manager (TAM) and Business Critical support for 24x7 live agent access.

Figure 11. NVIDIA AI Enterprise Software Stack
Support Information

Certification

- Windows Hardware Quality Lab (WHQL):
- Ergonomic requirements for office work W/VDTs (ISO 9241)
- EU Reduction of Hazardous Substances (EU RoHS)
- Joint Industry guide (J-STD) / Registration, Evaluation, Authorization, and Restriction of Chemical Substance (EU) – (JIG / REACH)
- Halogen Free (HF)
- EU Waste Electrical and Electronic Equipment (WEEE)

Agencies

- Australian Communications and Media Authority and New Zealand Radio Spectrum Management (RCM)
- Bureau of Standards, Metrology, and Inspection (BSMI)
- Conformité Européenne (CE)
- Federal Communications Commission (FCC)
- Industry Canada - Interference-Causing Equipment Standard (ICES)
- Korean Communications Commission (KCC)
- Underwriters Laboratories (cUL, UL)
- Voluntary Control Council for Interference (VCCI)
## Languages

### Table 9. Languages Supported

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<th>Languages</th>
<th>Windows¹</th>
<th>Linux</th>
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**Note:**

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