ACCELERATE DATA-DRIVEN SCIENTIFIC COMPUTING WITH IN-NETWORK COMPUTING

The NVIDIA® ConnectX®-7 NDR 400 gigabits per second (Gb/s) InfiniBand host channel adapter (HCA) provides the highest networking performance available to take on the world’s most challenging workloads. The ConnectX-7 InfiniBand adapter provides ultra-low latency, 400Gb/s throughput, and innovative NVIDIA In-Network Computing engines to deliver the acceleration, scalability, and feature-rich technology needed for high performance computing (HPC), artificial intelligence (AI), and hyperscale cloud data centers.

High performance computing and artificial intelligence have driven supercomputers into wide commercial use as the primary data processing engines enabling research, scientific discoveries and product development. These systems can carry complex simulations and unlock the new era of AI, where software writes software. NVIDIA InfiniBand networking is the engine of these platforms delivering breakthrough performance.

ConnectX-7 NDR InfiniBand smart In-Network Computing acceleration engines include collective accelerations, MPI Tag Matching and All-to-All engines, and programmable datapath accelerators. These performance advantages and the standard guarantee of backward- and forward-compatibility ensure leading performance and scalability for compute and data-intensive applications and enable users to protect their data center investments.

PORTFOLIO

- Single-port or dual-port NDR (400Gb/s) or NDR200 (200Gb/s), with octal small form-factor pluggable (OSFP) connectors
- Dual-port HDR (200Gb/s) with quad small form-factor pluggable (QSFP) connectors
- PCIe standup half-height, half-length (HHHL) and full-height, half-length (FHHL) form factors, with options for NVIDIA Socket Direct™
- Open Compute Project 3.0 (OCP3.0) tall small form factor (TSFF) and small form factor (SFF)
- Standalone ConnectX-7 application-specific integrated circuit (ASIC), supporting PCIe switch capabilities

PRODUCT SPECIFICATIONS

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max total bandwidth</td>
<td>400Gb/s</td>
</tr>
<tr>
<td>IBTA Spec compliant</td>
<td>1.5</td>
</tr>
<tr>
<td>Number of network ports</td>
<td>1/2/4</td>
</tr>
<tr>
<td>Host interface</td>
<td>PCIe Gen5, up to x32 lanes</td>
</tr>
<tr>
<td>RDMA message rate</td>
<td>330-370 million messages per second</td>
</tr>
<tr>
<td>Acceleration engines</td>
<td>Collective operations, MPI All-to-All, MPI tag matching, Programmable datapath accelerator</td>
</tr>
<tr>
<td>Advanced storage capabilities</td>
<td>Block level encryption and checksum offloads</td>
</tr>
<tr>
<td>Accurate timing</td>
<td>PTP 1558v2, 16ns accuracy</td>
</tr>
<tr>
<td>Secure boot</td>
<td>On-chip with hardware root-of-trust</td>
</tr>
<tr>
<td>Host management</td>
<td>NC-SI, MCTP over SMBus, and MCTP over PCIe</td>
</tr>
<tr>
<td>Supported operating systems</td>
<td>Linux, Windows, VMware</td>
</tr>
<tr>
<td>Form factors</td>
<td>PCIe HHHL, FHHL, Socket Direct OCP3.0 TSFF, SFF</td>
</tr>
</tbody>
</table>
FEATURES*

InfiniBand

- InfiniBand Trade Association (IBTA) Specification 1.5 compliant
- Up to four ports
- Remote direct-memory access (RDMA), send/receive semantics
- Hardware-based congestion control
- Atomic operations
- 16 million input/output (IO) channels
- 256 to 4Kbyte maximum transmission unit (MTU), 2Gbyte messages
- 8x virtual lanes (VL) + VL15

Enhanced Networking

- Hardware-based reliable transport
- Extended Reliable Connected (XRC) transport
- Dynamically Connected Transport (DCT)
- GPUDirect® RDMA
- GPUDirect Storage
- Out-of-order RDMA supporting adaptive routing
- Enhanced atomic operations
- Advanced memory mapping support, allowing user mode registration and remapping of memory (UMR)
- On-demand paging (ODP), including registration-free RDMA memory access
- Enhanced congestion control
- Burst buffer offload

In-Network Computing

- Collective operations offloads
- Vector collective operations offloads
- MPI tag matching
- MPI All-to-All offloads
- Rendezvous protocol offload
- In-network memory
- Programmable datapath accelerator

Hardware-Based IO Virtualization

- Single root IO virtualization (SR-IOV)

Storage Offloads

- Block-level encryption:
  - XTS-AES 256/512-bit key
- NVMe over Fabrics (NVMe-oF) offloads for target machine
- T10 Data Integrity Field (DIF) signature handover operation at wire speed for ingress and egress traffic
- Storage protocols: SRP, iSER, NFS RDMA, SMB Direct, NVMe-oF

HPC Software Libraries

- NVIDIA HPC-X® and UCX®, UCC, NCCL, OpenMPI, MVAPICH, MPICH, OpenSHMEM, PGAS, and various commercial packages

Management and Control

- NC-SI, MCTP over SMBus, and MCTP over PCIe
- PLDM for Monitor and Control DSP0248
- PLDM for Firmware Update DSP0267
- PLDM for Redfish Device Enablement DSP0218
- PLDM for FRU DSP0257
- SPDM DSP0274
- General-purpose IO pins
- Serial Peripheral Interface (SPI) to flash
- JTAG IEEE 1149.1 and IEEE 1149.6

Remote Boot

- Remote boot over InfiniBand
- Remote boot over Internet Small Computer Systems Interface (iSCSI)
- Unified Extensible Firmware Interface (UEFI)
- Preboot Execution Environment (PXE)

Security

- Secure boot with hardware root of trust
- Secure firmware update
- Flash encryption

Advanced Timing and Synchronization

- Advanced PTP
- IEEE 1588v2 (any profile)
- Line-rate hardware timestamp (UTC format)
- Configurable PPS In and configurable PPS Out
- Time-triggered scheduling
- PTP-based packet pacing
- Time-Sensitive Networking (TSN)

COMPATIBILITY

PCI Express Interface

- PCIe Gen 5.0 compatible, 32 lanes
- Support for PCIe x1, x2, x4, x8, and x16 configurations
- NVIDIA Multi-Host™ supports connection of up to 8x hosts
- PCIe Atomic
- Transaction layer packet (TLP) processing hints (TPH)
- PCIe switch Downstream Port Containment (DPC)
- Advanced error reporting (AER)
- Access Control Service (ACS) for peer-to-peer secure communication
- Process Address Space ID (PASID)
- Address translation services (ATS)
- Support for MSI/MSI-X mechanisms
- Support for SR-IOV

Operating Systems/Distributions*

- In-box drivers for major operating systems:
  - Linux: RHEL, Ubuntu
  - Windows
- Virtualization and containers
  - VMware ESXi (SR-IOV)
- Kubernetes
- OpenFabrics Enterprise Distribution (OFED)
- OpenFabrics Windows Distribution (WinOF-2)

*This section describes hardware features and capabilities. Please refer to the driver and firmware release notes for feature availability.
## ADAPTER CARD PORTFOLIO AND ORDERING INFORMATION

### PCIE STANDUP FORM FACTOR

<table>
<thead>
<tr>
<th>InfiniBand Supported Speeds [Gb/s]</th>
<th>Network Ports and Cages</th>
<th>Host Interface [PCIe]</th>
<th>Form Factor / Feature Support</th>
<th>Orderable Part Number (OPN)</th>
</tr>
</thead>
<tbody>
<tr>
<td>NDR/NDR200</td>
<td>1x OSFP</td>
<td>PCIe Gen 4.0/5.0 x16</td>
<td>HHHL</td>
<td>MCX75510AAN-NEAT</td>
</tr>
<tr>
<td></td>
<td>1x OSFP</td>
<td>With option for extension</td>
<td>HHHL</td>
<td>MCX75310AAN-NEAT</td>
</tr>
<tr>
<td></td>
<td>1x OSFP</td>
<td>PCIe Gen 4.0/5.0 x16</td>
<td>HHHL</td>
<td>MCX75510AAS-NEAT</td>
</tr>
<tr>
<td></td>
<td>2x OSFP</td>
<td>PCIe Gen 4.0/5.0 x16</td>
<td>Secure boot</td>
<td>MCX75511BAN-NEAT</td>
</tr>
<tr>
<td>NDR200</td>
<td>1x OSFP</td>
<td>PCIe Gen 4.0/5.0 x16</td>
<td>HHHL</td>
<td>MCX75510AAN-HEAT</td>
</tr>
<tr>
<td></td>
<td>1x OSFP</td>
<td>With option for extension</td>
<td>HHHL</td>
<td>MCX75310AAN-HEAT</td>
</tr>
<tr>
<td></td>
<td>2x OSFP</td>
<td>PCIe Gen 4.0/5.0 x16</td>
<td>HHHL</td>
<td>MCX75511BAN-HEAT</td>
</tr>
<tr>
<td>HDR/HDR100/EDR</td>
<td>1x OSFP</td>
<td>PCIe Gen 4.0/5.0 x16</td>
<td>HHHL</td>
<td>MCX755105AN-HEAT</td>
</tr>
<tr>
<td></td>
<td>2x OSFP</td>
<td>With option for extension</td>
<td>HHHL</td>
<td>MCX755106AN-HEAT</td>
</tr>
</tbody>
</table>

Dimensions without brackets are 167.65mm x 68.90mm. All adapters are shipped with the tall bracket mounted and a short bracket as an accessory.

### OCP 3.0 SMALL FORM FACTOR

<table>
<thead>
<tr>
<th>InfiniBand Supported Speeds [Gb/s]</th>
<th>Network Ports and Cages</th>
<th>Host Interface [PCIe]</th>
<th>OPN</th>
</tr>
</thead>
<tbody>
<tr>
<td>NDR/NDR200/</td>
<td>1x OSFP</td>
<td>PCIe Gen 4.0/5.0 x16 TSFF</td>
<td>MCX75343AAN-NEAB¹</td>
</tr>
<tr>
<td>HDR/HDR100</td>
<td>2x QSFP56</td>
<td>PCIe Gen 4.0/5.0 x16 SFF</td>
<td>MCX753436AN-HEAB</td>
</tr>
</tbody>
</table>

The last digit of the OPN-suffix displays the default bracket option: B = pull tab, I = internal lock; E = ejector latch.

For other bracket types, contact NVIDIA.

Note 1: Pre OCP3.2 Spec

### IC ORDERING INFORMATION

<table>
<thead>
<tr>
<th>Product Description</th>
<th>OPN</th>
</tr>
</thead>
<tbody>
<tr>
<td>ConnectX-7 2-port IC, NDR, PCIe 5.0 x32, No Crypto</td>
<td>MT29108A0-NCCF-NV</td>
</tr>
<tr>
<td>ConnectX-7 2-port IC, NDR, Multi-Host, PCIe 5.0 x32, No Crypto</td>
<td>MT29108A0-NCCF-NVM</td>
</tr>
<tr>
<td>ConnectX-7 2-port IC, NDR, Multi-Host, PCIe 5.0 x32, Crypto</td>
<td>MT29108A0-CCCF-NVM</td>
</tr>
</tbody>
</table>

Learn more at [NVIDIA ConnectX-7 InfiniBand](#)