

POWERING CHANGE WITH AI | NVIDIA

USING AI TO REVEAL INSIGHTS IN ENTERPRISE LOG FILE DATA



Image courtesy of NVIDIA



NVIDIA DGX Systems and AI help businesses zero in on problems before they surface.

INTRODUCTION

Businesses in every industry run on processes that generate oceans of log file data. Modern enterprises spend inordinate amounts of time and resources scouring machine or process log files to inspect error conditions, which often include false positives. This drives up costs and reduces agility in anticipating and responding to actual business-impacting conditions.

Enterprises are leveraging AI-based log analysis to prevent these costly mistakes: Security teams can analyze logs for anomalies and prevent breaches before they happen; manufacturing departments can detect failures before they spread to the next part of an assembly line and purchase parts before they wear down, reducing unplanned downtime; and IT operations—bombarded with logs from all aspects of the system, including network, web server, and database—can now troubleshoot performance issues before they become problems. In some industries like financial services, this could save millions of dollars, as network issues and unwanted latency can delay the completion of trades, causing missed arbitrage opportunities. At the same time, AI-based log analysis is being used to uncover missed revenue opportunities by analyzing customer transaction logs in real time to offer upsell opportunities or incentives that prevent transactions from being abandoned.

AI and deep learning are invaluable tools that can help organizations derive real-time insights from log file data. These insights can lead to increased productivity, fewer false positives, and faster responses to problems before they surface. In this case study, we explore how NVIDIA has implemented AI-powered log file analysis to streamline the development of the world's most advanced GPUs.

CHALLENGE

Designing the GPU is a very complicated task. NVIDIA engineers must ensure that the connections between each and every functional block—including billions of transistors—work properly. Circuit verification and emulation occur to test the functional blocks, with each block undergoing thousands of tests.

Often, the log files generated by these tests have their own cryptic language, and overreporting on errors is common; the word “error” will appear many times in the file, even when the events are completely benign, resulting in many false positives.

In physical design, the design process kicks off thousands of runs every day; an average of 15 percent of those runs fail for various reasons. That can amount to thousands of failed log files that a team of engineers have to go through—and each file can have millions of lines of code. For an experienced engineer, it typically takes ten to fifteen minutes to figure out the problem. In many cases, it can take much longer. Just five minutes of debugging time saved for each log file would save well over a hundred engineering hours per day, or more than two million dollars in operating expenses per year.

“We’ve significantly reduced debugging time and given well over a hundred hours per day of engineering time back using the PyTorch deep learning framework powered by NVIDIA DGX-1 computing.”

Senior Software Engineer,
Physical Design
NVIDIA

RESULTS

With the trained model, the next step was real-time inference. The team was able to easily set up an inference pipeline, which included NVIDIA V100 GPUs. Without NVIDIA DGX and its container management and resource allocation tools, this would not have been possible.

Every five minutes, lines from log files are sent to the inference pipeline. They no longer need to wait until the job run is complete and the log file is generated, which can sometimes take overnight. Instead, they send the lines to the inference server as they’re being added to the log file. Any flagged anomalous lines are fed into the database. The team retrains the model every week for about four hours, so the model can learn from the latest grammar. “We developed a method that requires practically zero user input. Our system can detect as things are changing and will retrain itself as log files with new grammar are fed in,” says a senior software engineer at NVIDIA. “We’ve significantly reduced debugging time and given well over a hundred hours per day of engineering time back using the PyTorch deep learning framework powered by NVIDIA DGX-1 computing.”

LOOKING AHEAD

In the future, the team is looking to deliver several feature enhancements to the model, including the ability to classify or group each new error. This way, the engineers only need to debug that type of error once. The team also wants to create a pipeline that lets their users provide solutions to issues. With this in place, when a new error is encountered, the system would automatically search the solution pool to provide relevant suggestions on how to fix the error. And beyond the reasons for failures, the team is implementing ways to analyze the log files for other reasons, such as quality of runs.

“While NVIDIA’s physical design team spent about 10 percent of their time debugging prior to using deep learning, today, that’s been reduced to 5 percent. We anticipate that this will even further decrease as we evolve our model and more teams look to adopt the same solution,” says the NVIDIA engineer. “Industry wide, the impact of using this methodology is huge—on average, software developers spend 35-50 percent of their time validating and debugging software. The cost of debugging, testing, and verification is estimated to account for 50-75 percent of the total budget of software development projects, amounting to more than \$100 billion annually.”¹

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**Director of Engineering
for VLSI
NVIDIA**

“Almost every single company in the world running servers have hosts of applications that generate massive log files. Using AI to automatically recognize valid errors versus false positives amongst an ocean of process log files delivers tremendous savings in time and employee efficiency, while increasing business agility,” says NVIDIA’s director of engineering for very-large-scale integration (VLSI).

Beyond the design team, the testing team is also looking to adopt the model. GPUs are stress-tested to understand how fast they can run. The test involves making them ray trace various images, applying reflections to reflective surfaces like water, glass, mirrors, and metal. At a certain point when the GPU has been stressed too far, pixelation of the image occurs. Currently, the testing team manually looks at each picture, which is very time consuming. They’re looking to apply the same deep learning methodology to train the model to recognize a good versus bad image and understand what each picture should look like based on the pixels surrounding it. This would significantly speed up testing time and remove subjectivity.

www.nvidia.com/dgx



¹ O'Dell, Devon H. "The Debugging Mindset: Understanding the Psychology of Learning Strategies Leads to Effective Problem-Solving Skills." *Association for Computing Machinery*. March 22, 2017.

